

EEE PC 701 PCB version

GPI37	GPI38	GPI39	PCB version
0	0	0	
0	0	0	
0	0	1	
0	0	1	
0	1	0	
0	1	0	
0	1	1	
0	1	1	
1	0	0	
1	0	0	
1	0	1	
1	0	1	
1	1	0	
1	1	0	
1	1	1	
1	1	1	

USB

USB 0	USB Conn
USB 1	USB Conn
USB 2	NA
USB 3	NA
USB 4	Card Reader
USB 5	Minicard
USB 6	Bluetooth
USB 7	Camera

PCIE

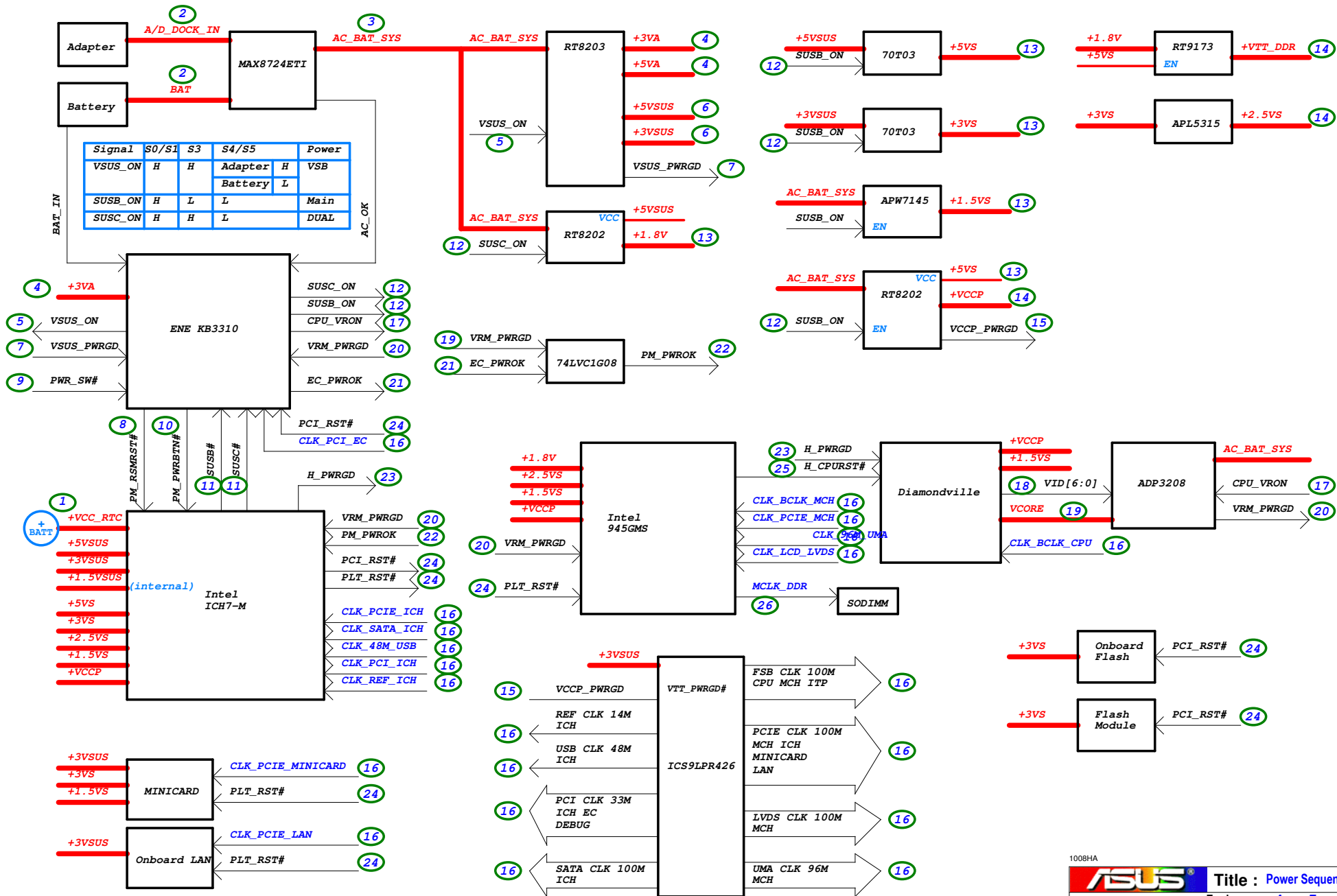
PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	SSD

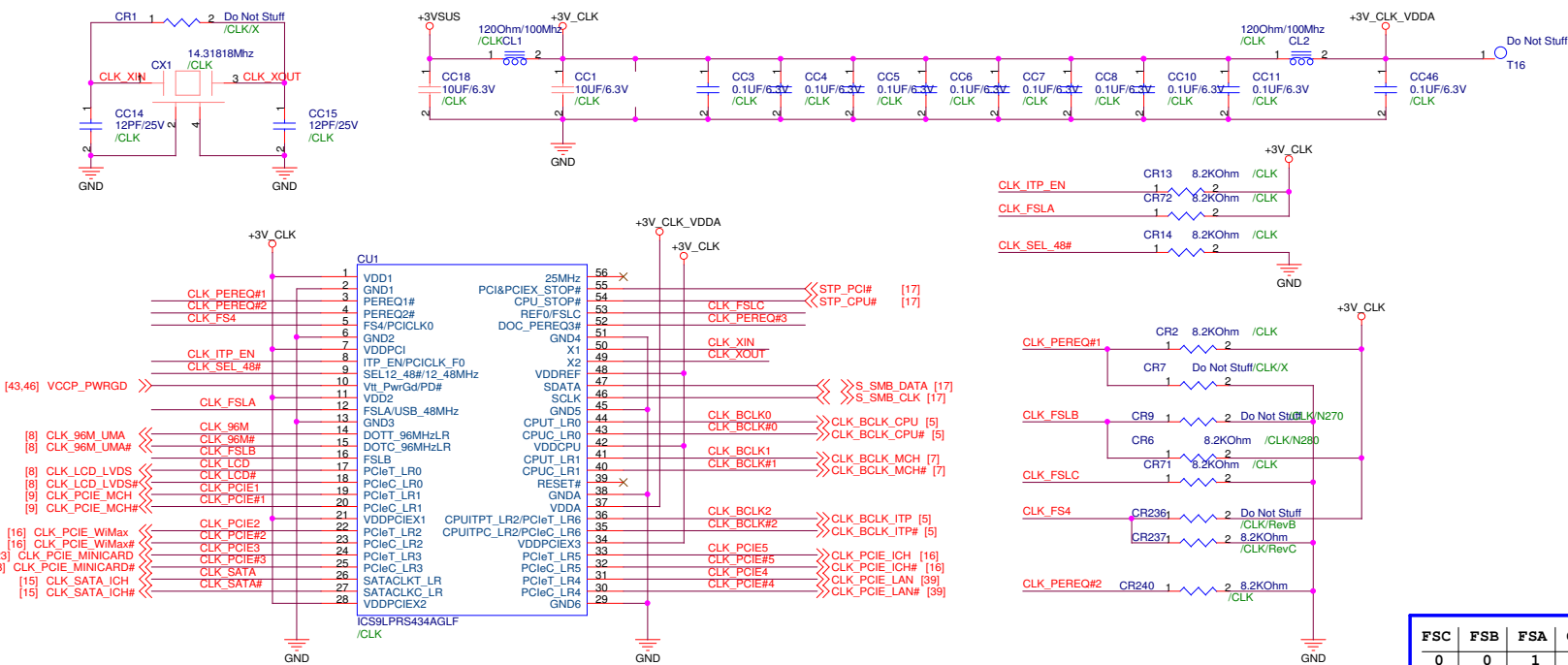
Azalia

ACZ_SDIN0	CODEC
ACZ_SDIN1	NC
ACZ_SDIN2	NC

1008HA

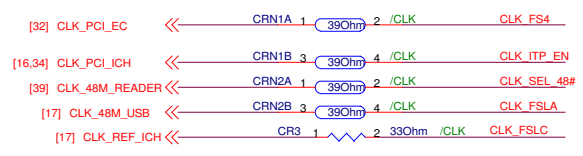
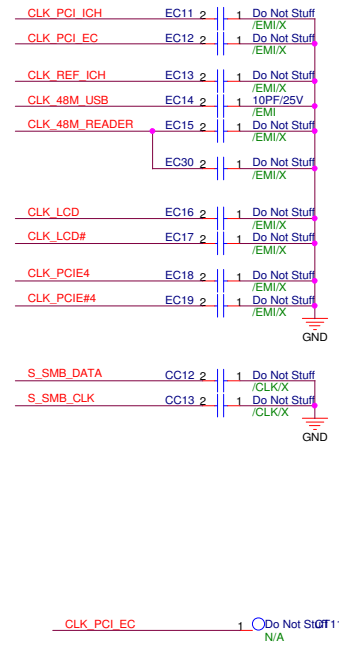
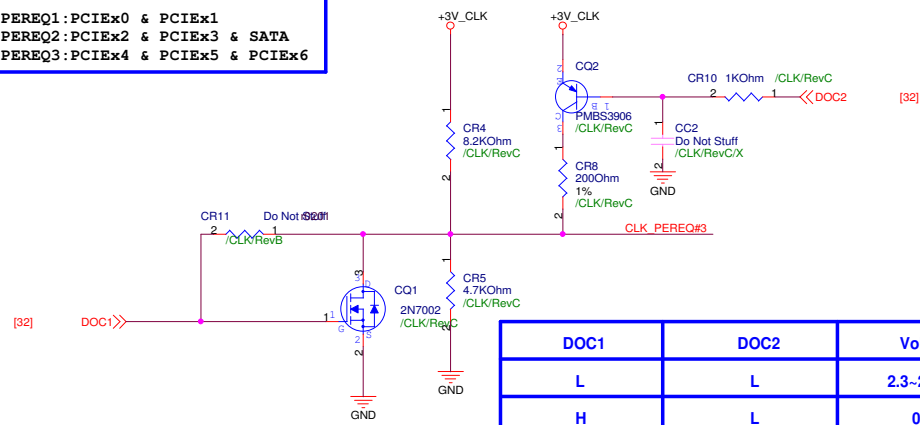
		Title : System Setting	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 2 of 49	

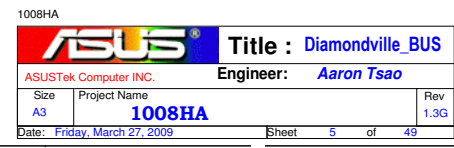




Selected through the I2C
1:Disable
0:Enable

PEREQ1:PCIEx0 & PCIEx1
PEREQ2:PCIEx2 & PCIEx3 & SATA
PEREQ3:PCIEx4 & PCIEx5 & PCIEx6





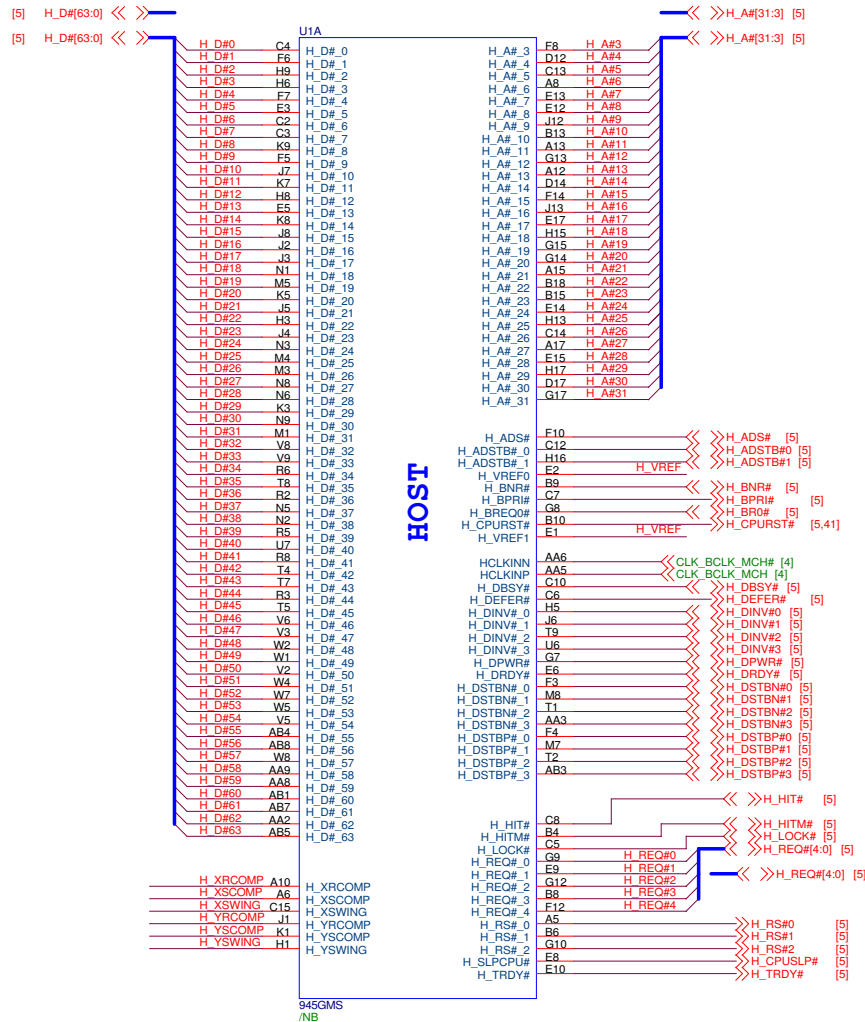
RCOMP

H XRCOMP



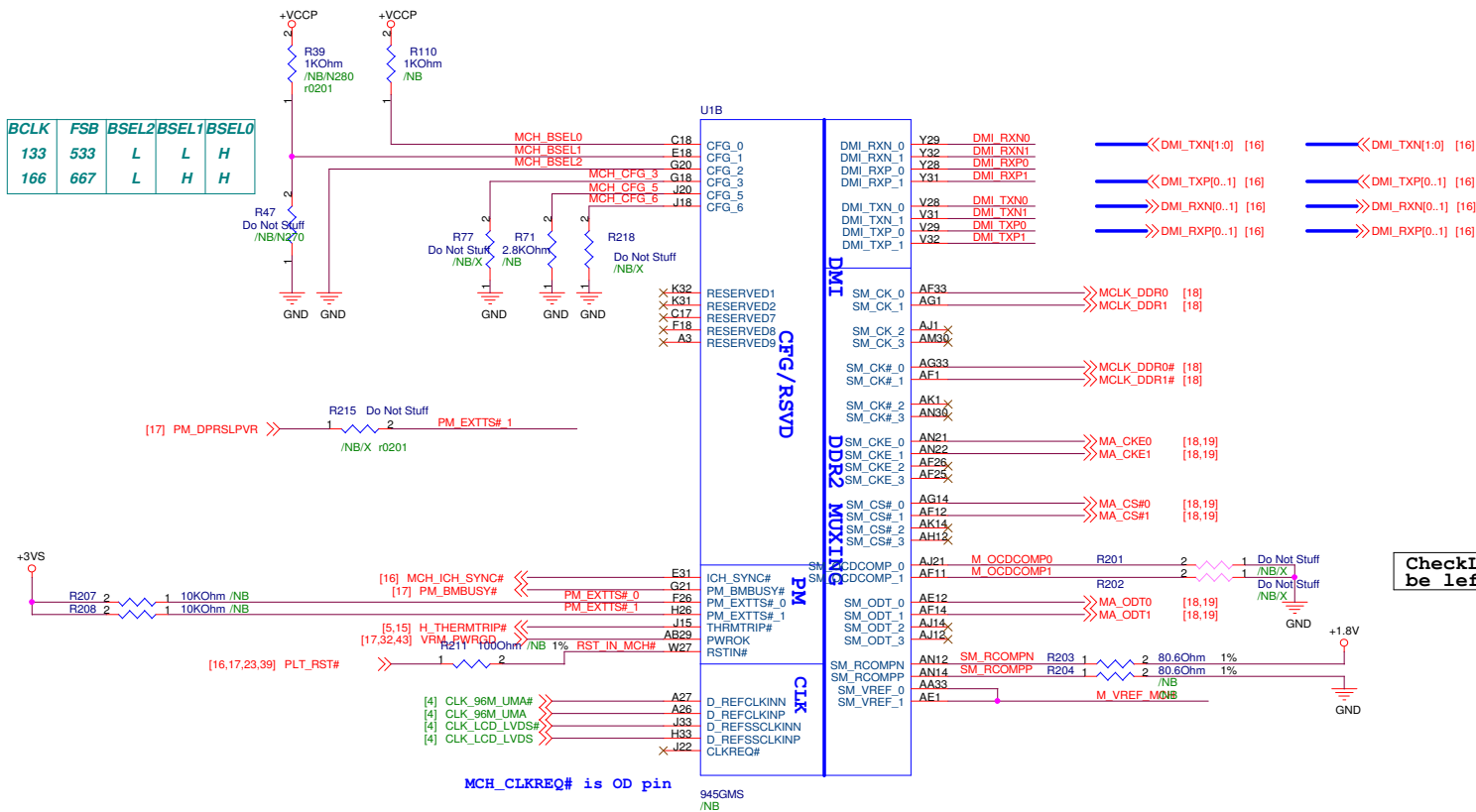
Voltage Swing

Timing diagram for CLK_BCLK_MCH# [4]. The diagram shows two pulses, C18 and C19, both labeled "Do Not Stuff /NB/X". Both pulses are connected to a GND symbol.



Layout Note:
0.1uF should be placed 100mils or less from GMCH pin.

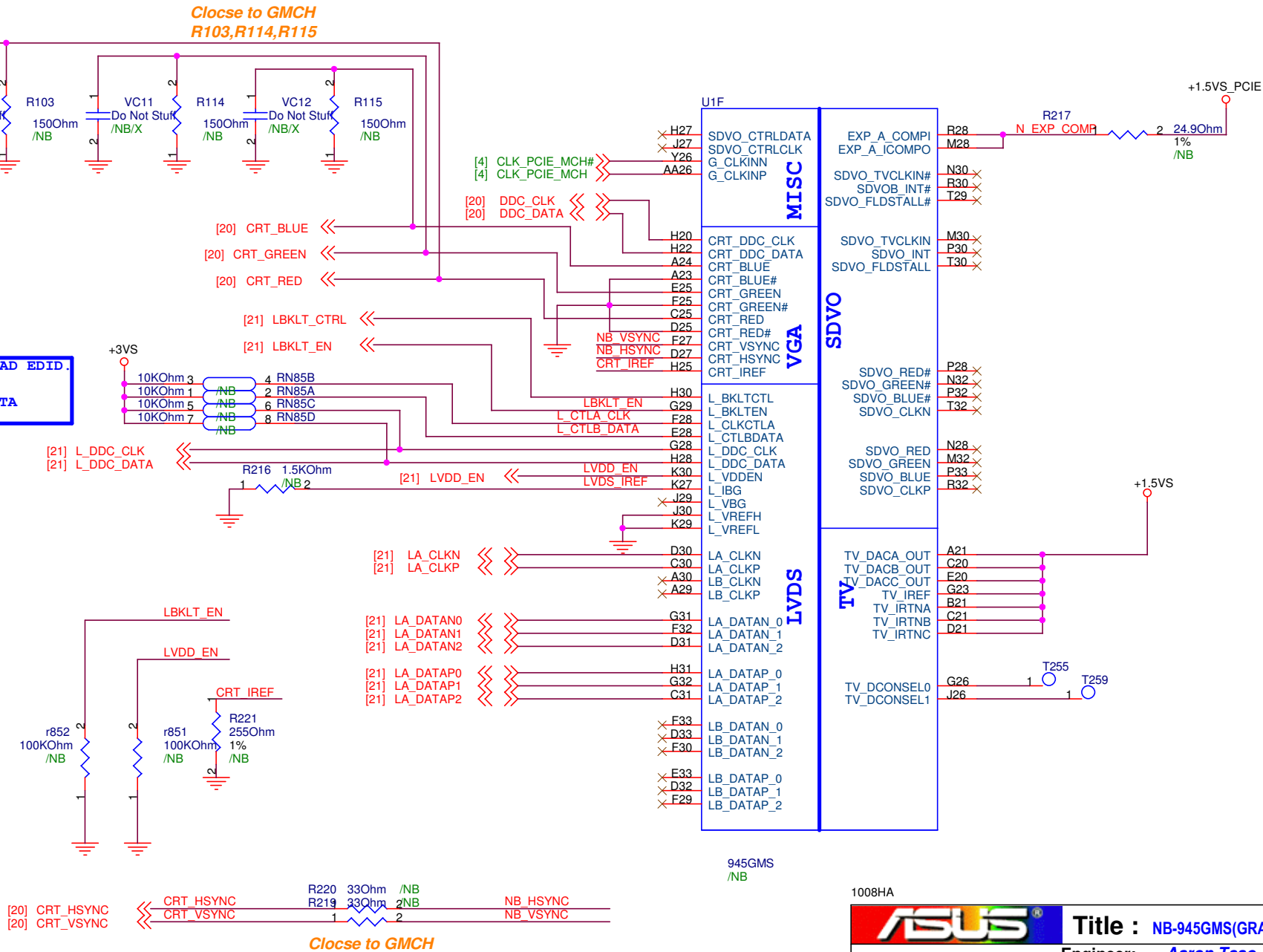
BCLK	FSB	BSEL2	BSEL1	BSEL0
133	533	L	L	H
166	667	L	H	H



1008HA

ASUS		Title : NB-945GMS(DMI & CFG)	
ASUSTeK COMPUTER INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 8 of 49	

IF USE NB READ EDID.
MUST CONNECT
L_DDC_CLK&DATA



[18] MA_DQ[63:0] << >>
[18] MA_DQ[63:0] << >>

U1C
MA_DQ0 AC31 SA_DQ_0
MA_DQ1 AB28 SA_DQ_1
MA_DQ2 AE32 SA_DQ_2
MA_DQ3 AC32 SA_DQ_3
MA_DQ4 AC33 SA_DQ_4
MA_DQ5 AB32 SA_DQ_5
MA_DQ6 AB31 SA_DQ_6
MA_DQ7 AE31 SA_DQ_7
MA_DQ8 AH31 SA_DQ_8
MA_DQ9 AK31 SA_DQ_9
MA_DQ10 AL28 SA_DQ_10
MA_DQ11 AL28 SA_DQ_11
MA_DQ12 AH30 SA_DQ_12
MA_DQ13 AL32 SA_DQ_13
MA_DQ14 AJ28 SA_DQ_14
MA_DQ15 AJ27 SA_DQ_15
MA_DQ16 AH32 SA_DQ_16
MA_DQ17 AE31 SA_DQ_17
MA_DQ18 AH27 SA_DQ_18
MA_DQ19 AE28 SA_DQ_19
MA_DQ20 AJ32 SA_DQ_20
MA_DQ21 AG31 SA_DQ_21
MA_DQ22 AG28 SA_DQ_22
MA_DQ23 AG27 SA_DQ_23
MA_DQ24 AN27 SA_DQ_24
MA_DQ25 AN26 SA_DQ_25
MA_DQ26 AJ26 SA_DQ_26
MA_DQ27 AJ25 SA_DQ_27
MA_DQ28 AL27 SA_DQ_28
MA_DQ29 AN26 SA_DQ_29
MA_DQ30 AH25 SA_DQ_30
MA_DQ31 AG26 SA_DQ_31
MA_DQ32 AM12 SA_DQ_32
MA_DQ33 AL11 SA_DQ_33
MA_DQ34 AH9 SA_DQ_34
MA_DQ35 AK9 SA_DQ_35
MA_DQ36 AM11 SA_DQ_36
MA_DQ37 AK11 SA_DQ_37
MA_DQ38 AM8 SA_DQ_38
MA_DQ39 AK8 SA_DQ_39
MA_DQ40 AG9 SA_DQ_40
MA_DQ41 AF9 SA_DQ_41
MA_DQ42 AF8 SA_DQ_42
MA_DQ43 AK6 SA_DQ_43
MA_DQ44 AF7 SA_DQ_44
MA_DQ45 AG11 SA_DQ_45
MA_DQ46 AJ6 SA_DQ_46
MA_DQ47 AH6 SA_DQ_47
MA_DQ48 AN6 SA_DQ_48
MA_DQ49 AM6 SA_DQ_49
MA_DQ50 AK3 SA_DQ_50
MA_DQ51 AL2 SA_DQ_51
MA_DQ52 AM5 SA_DQ_52
MA_DQ53 AL5 SA_DQ_53
MA_DQ54 AJ3 SA_DQ_54
MA_DQ55 AJ2 SA_DQ_55
MA_DQ56 AG2 SA_DQ_56
MA_DQ57 AF3 SA_DQ_57
MA_DQ58 AE7 SA_DQ_58
MA_DQ59 AF6 SA_DQ_59
MA_DQ60 AH5 SA_DQ_60
MA_DQ61 AG3 SA_DQ_61
MA_DQ62 AG5 SA_DQ_62
MA_DQ63 AF5 SA_DQ_63

AG19 SB_CAS#
AG21 SB_RAS#
AG20 SB_WE#

945GMS
/NB

DDR2 SYSTEM MEMORY

SA_BS_0 AK12
SA_BS_1 AH11
SA_BS_2 AG17
SA_DM_0 AB30 MA_DM0
SA_DM_1 AL31 MA_DM1
SA_DM_2 AF30 MA_DM2
SA_DM_3 AK26 MA_DM3
SA_DM_4 AL9 MA_DM4
SA_DM_5 AG7 MA_DM5
SA_DM_6 AK5 MA_DM6
SA_DM_7 AH3 MA_DM7
SA_DQS_0 AC28 MA_DQS0
SA_DQS_1 AJ30 MA_DQS1
SA_DQS_2 AK33 MA_DQS2
SA_DQS_3 AL25 MA_DQS3
SA_DQS_4 AN9 MA_DQS4
SA_DQS_5 AH8 MA_DQS5
SA_DQS_6 AM2 MA_DQS6
SA_DQS_7 AE3 MA_DQS7
SA_DQS#_0 AC29 MA_DQS#0
SA_DQS#_1 AK30 MA_DQS#1
SA_DQS#_2 AJ33 MA_DQS#2
SA_DQS#_3 AH25 MA_DQS#3
SA_DQS#_4 AN8 MA_DQS#4
SA_DQS#_5 AJ8 MA_DQS#5
SA_DQS#_6 AM3 MA_DQS#6
SA_DQS#_7 AE2 MA_DQS#7
SA_MA_0 AJ15 MA_MA0
SA_MA_1 AM17 MA_MA1
SA_MA_2 AM15 MA_MA2
SA_MA_3 AH15 MA_MA3
SA_MA_4 AK15 MA_MA4
SA_MA_5 AN15 MA_MA5
SA_MA_6 AJ18 MA_MA6
SA_MA_7 AF19 MA_MA7
SA_MA_8 AN17 MA_MA8
SA_MA_9 AL17 MA_MA9
SA_MA_10 AG16 MA_MA10
SA_MA_11 AL18 MA_MA11
SA_MA_12 AG18 MA_MA12
SA_MA_13 AL14 MA_MA13
SA_CAS# AJ17
SA_RAS# AK18
SA_RCVENIN# AN28
SA_RCVENOUT# AM28
AH17
AH21
AJ20
AE27
AN28
AL21
AK21
AK22
AL23
AH22
AQ22
AF21
AM21
AE21
AL20
AE22
AE28
AE20

<< >> MA_DQS[7:0] [18]
<< >> MA_DM[7:0] [18]

>> MA_BA0 [18,19]
>> MA_BA1 [18,19]
>> MA_BA2 [18,19]

<< >> MA_DQS# [7:0] [18]

<< >> MA_MA [13:0] [18,19]

>> MA_CAS# [18,19]
>> MA_RAS# [18,19]
1 T223
>> MA_WE# [18,19]

U1G

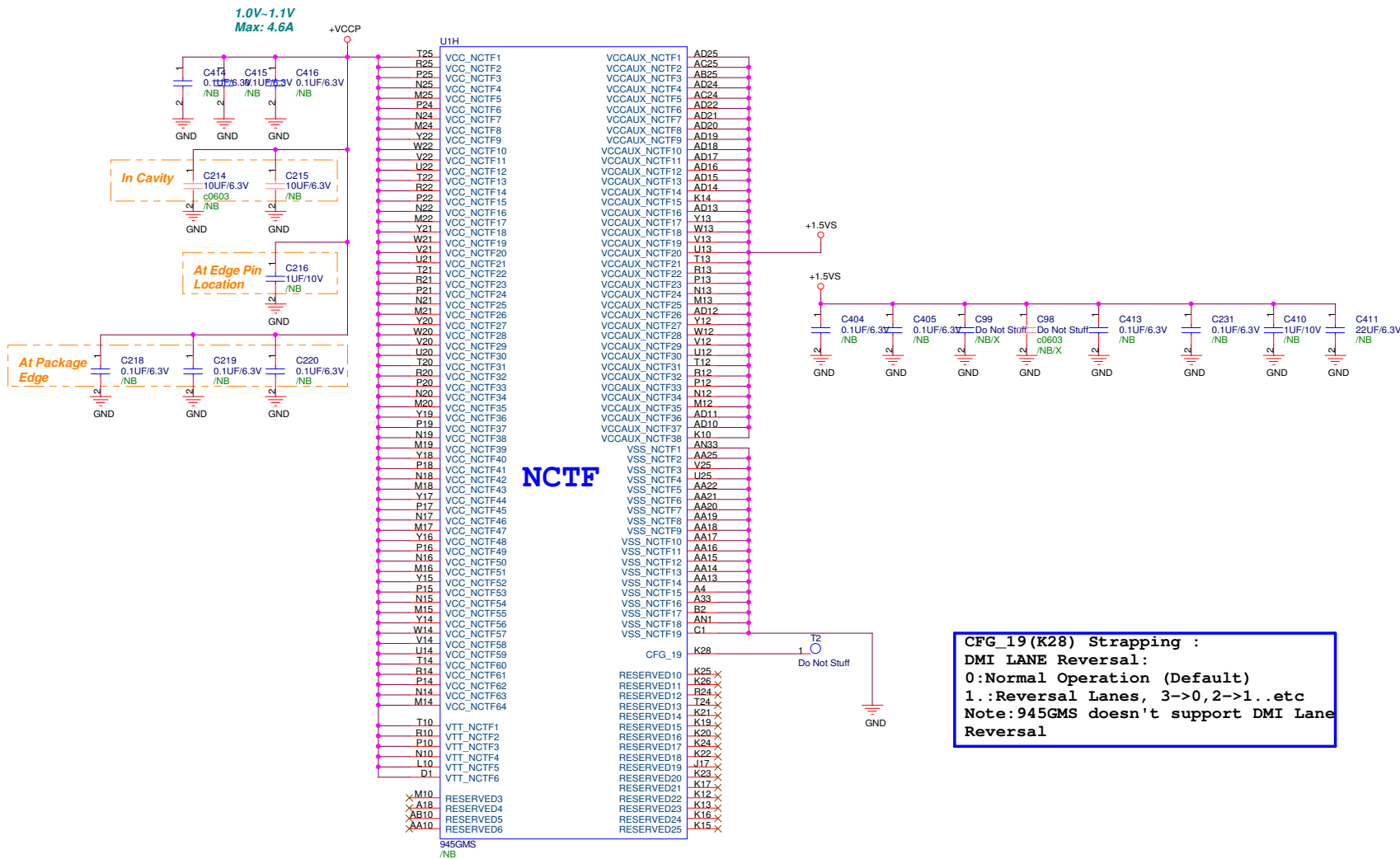
W33 NC1
AM33 NC2
AL33 NC3
C33 NC4
B33 NC5
AN32 NC6
A32 NC7
AN31 NC8
W28 NC9
W27 NC10
W25 NC11
J24 NC12
H24 NC13
W32 NC14
G24 NC15
E24 NC16
D24 NC17
K33 NC18
A31 NC19
E21 NC20
C23 NC21
AN19 NC22
AM19 NC23
AL19 NC24
AK19 NC25
AJ19 NC26
AH19 NC27
AN3 NC28
Y9 NC29
J19 NC30
H19 NC31
G19 NC32
E19 NC33
D19 NC34
C19 NC35
B19 NC36
A19 NC37
Y8 NC38
G16 NC39
E16 NC40
D16 NC41
C16 NC42
B16 NC43
A16 NC44
Y7 NC45
G16 NC46
E16 NC47
D16 NC48
C16 NC49
B16 NC50
A16 NC51
Y7 NC52
G16 NC53
E16 NC54
D16 NC55
C16 NC56
B16 NC57
A16 NC58
Y7 NC59
G16 NC60
RESERVED26 Y25
RESERVED27 Y24
RESERVED28 AB22
RESERVED29 AB21
RESERVED30 AB19
RESERVED31 AB16
RESERVED32 AB14
RESERVED33 AA14
RESERVED34 W24
RESERVED35 AA24
RESERVED36 AB24
RESERVED37 AB20
RESERVED38 AB18
RESERVED39 AB15
RESERVED40 AB13
RESERVED41 AB12
RESERVED42 AB11

NC

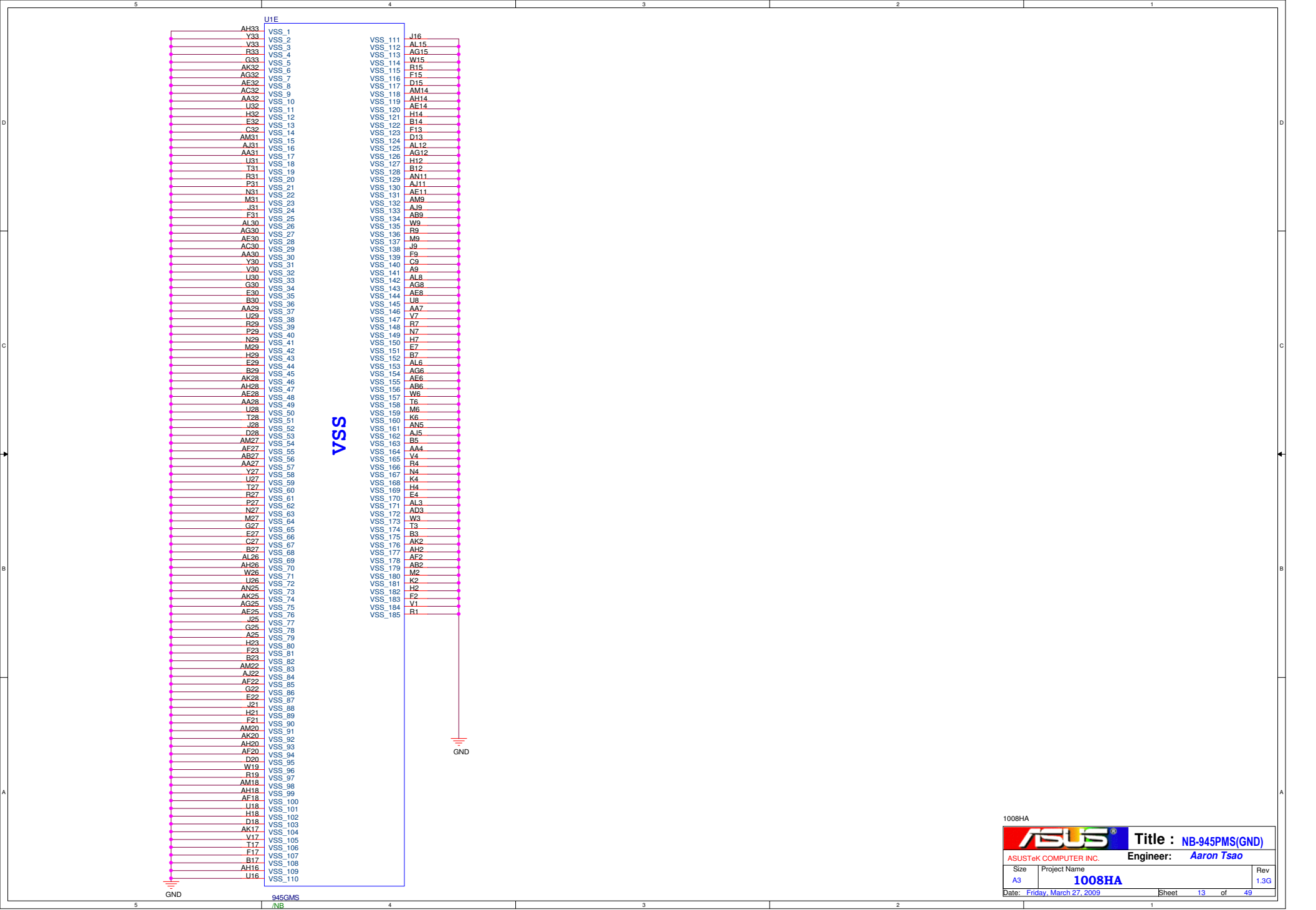
945GMS
/NB

1008HA

ASUS		Title : NB-945GMS(DDR2)	
ASUSTeK COMPUTER INC.		Engineer: Aaron Tsao	
Size	Project Name	Rev	
A3	1008HA	1.3G	
Date: Friday, March 27, 2009	Sheet 10 of 49		



1008HA

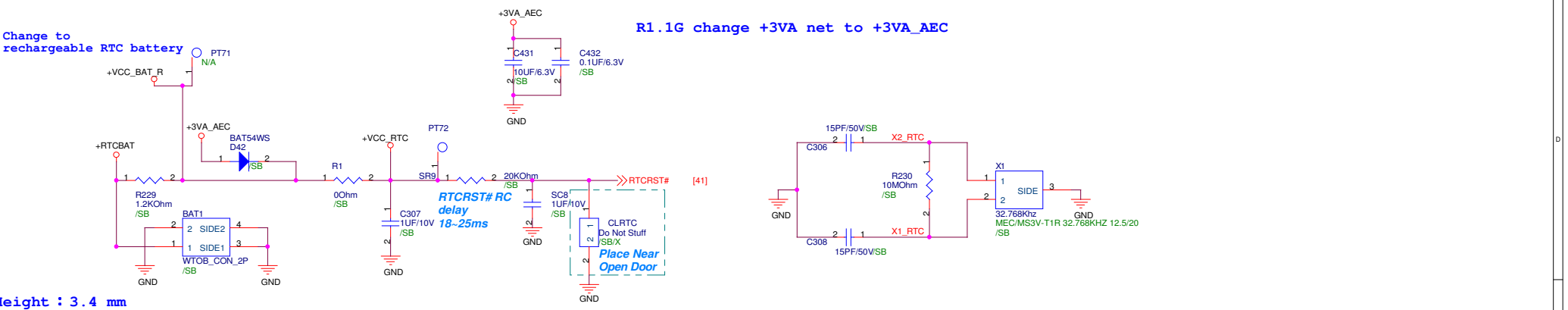


1008HA

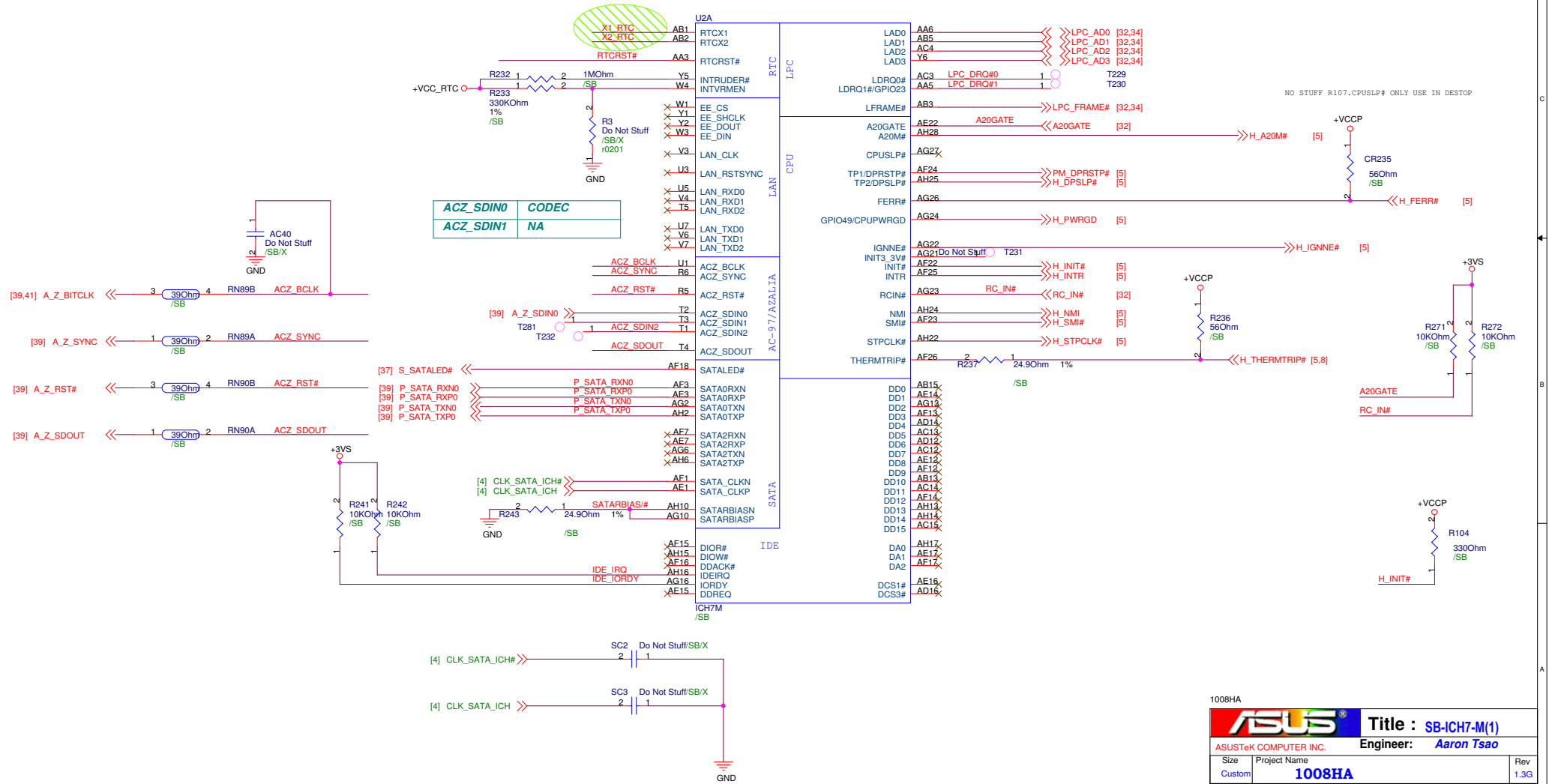
		Title : NB-945PMS(GND)	
ASUSTeK COMPUTER INC.		Engineer: Aaron Tsao	
Size	Project Name		Rev
A3	1008HA		1.3G
Date: Friday, March 27, 2009		Sheet	13 of 49

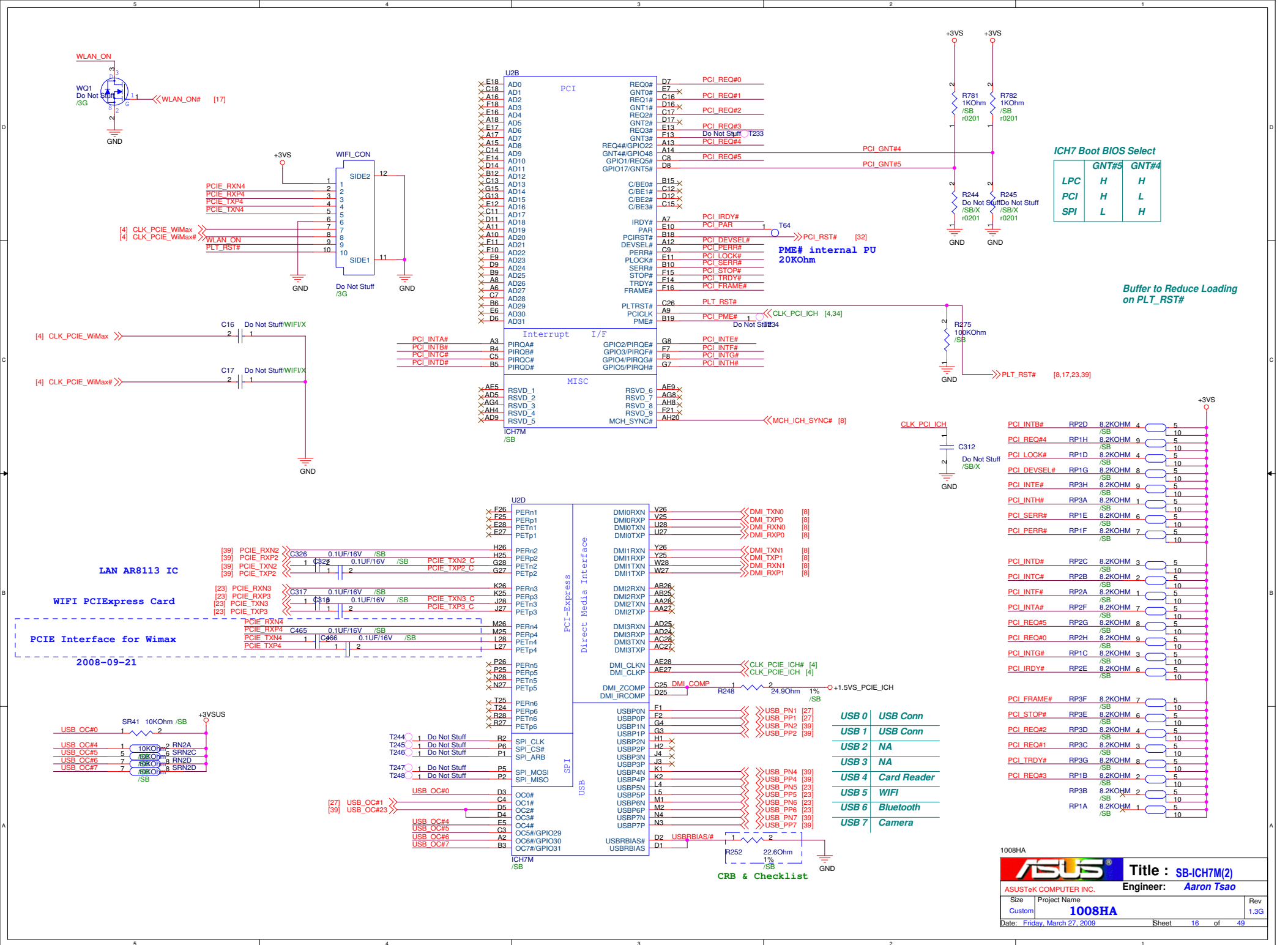


Change to rechargeable RTC battery



Height : 3.4 mm







Isolate alert# signal from thermal IC

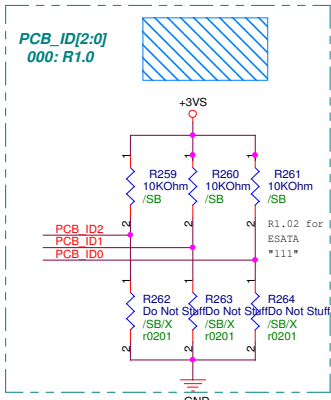
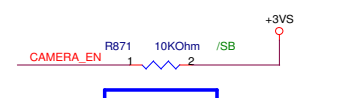
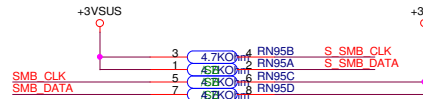
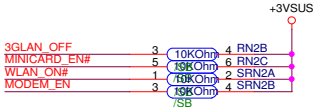
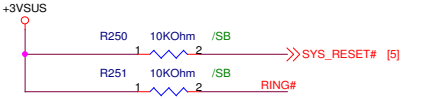


05/12/30, refer 296J R1.01 to delete and change net name from VRMPWRGD to VRM_PWRGD.

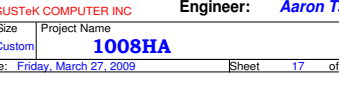
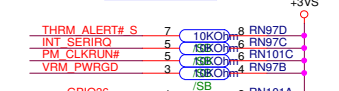
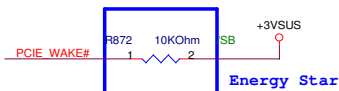
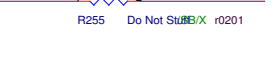
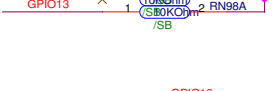
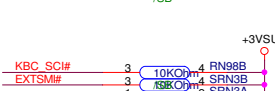
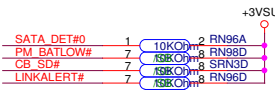
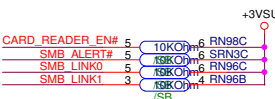
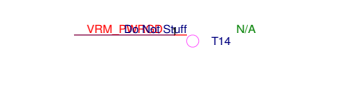
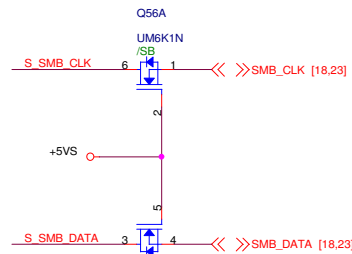
S SMB_CLK >>> S SMB_CLK [4]
S SMB_DATA >>> S SMB_DATA [4]

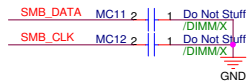
WLAN_LED	WLAN	BT
High	v	v
High	v	x
High	x	v
Low	x	x

GPIO25 Internal PU 20K For +1.5V DIMM Power



PCB_VID3 : PROJECT CODE





<< >>MA_DQ[63:0] [10]
<< >>MA_DQS[7:0] [10]
<< >>MA_DQS#[7:0] [10]
<< >>MA_DM[7:0] [10]
<< >>MA_MA[13:0] [10,19]
<< >>MA_BA[2:0] [10,19]

STD Type

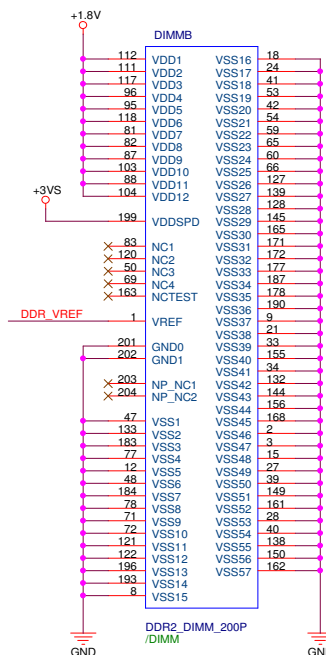
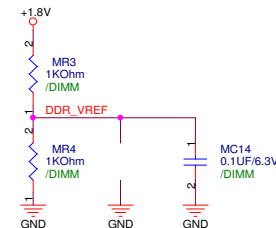
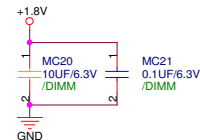
DDR2 Conn. Height=4.0mm

GROUP1
GROUP2
SWAP

DIMMA			
MA_MA0	102	A0	DQ0
MA_MA1	101	A1	DQ1
MA_MA2	100	A2	DQ2
MA_MA3	99	A3	DQ3
MA_MA4	98	A4	DQ4
MA_MA5	97	A5	DQ5
MA_MA6	94	A6	DQ6
MA_MA7	92	A7	DQ7
MA_MA8	91	A8	DQ8
MA_MA9	90	A9	DQ9
MA_MA10	105	A10/AP	DQ10
MA_MA11	90	A11	DQ11
MA_MA12	89	A12	DQ12
MA_MA13	116	A13	DQ13
	86	A14	DQ14
	84	A15	DQ15
MA_BA2	85	A16_BA2	DQ16
MA_BA0	107	BA0	DQ17
MA_BA1	106	BA1	DQ18
	110	SA0	DQ19
[8,19] MA_CS#0		S1#	DQ20
[8,19] MA_CS#1		S1#	DQ21
	30	CK0	DQ22
[8] MCLK_DDR0		CK1#	DQ23
[8] MCLK_DDR0#		CK1#	DQ24
[8] MCLK_DDR1		CK1#	DQ25
[8] MCLK_DDR1#		CK1#	DQ26
[8,19] MA_CKE0		CKE0	DQ27
[8,19] MA_CKE1		CKE1	DQ28
[10,19] MA_CAS#		CAS#	DQ29
[10,19] MA_RAS#		RAS#	DQ30
[10,19] MA_WE#		WE#	DQ31
	198	SA0	DQ32
	200	SA1	DQ33
[17,23] SMB_CLK		SCL	DQ34
[17,23] SMB_DATA		SDA	DQ35
	114	ODT0	DQ36
[8,19] MA_ODT0		ODT1	DQ37
[8,19] MA_ODT1		ODT1	DQ38
	10	DM0	DQ39
MA_DM0		DM1	DQ40
MA_DM1		DM2	DQ41
MA_DM2		DM3	DQ42
MA_DM3		DM4	DQ43
MA_DM4		DM5	DQ44
MA_DM5		DM6	DQ45
MA_DM6		DM7	DQ46
MA_DM7		DM7	DQ47
	13	DQS0	DQ48
MA_DQS0		DQS1	DQ49
MA_DQS1		DQS2	DQ50
MA_DQS2		DQS3	DQ51
MA_DQS3		DQS4	DQ52
MA_DQS4		DQS5	DQ53
MA_DQS5		DQS6	DQ54
MA_DQS6		DQS7	DQ55
MA_DQS7		DQS8	DQ56
MA_DQS8		DQS9	DQ57
MA_DQS9		DQS10	DQ58
MA_DQS10		DQS11	DQ59
MA_DQS11		DQS12	DQ60
MA_DQS12		DQS13	DQ61
MA_DQS13		DQS14	DQ62
MA_DQS14		DQS15	DQ63

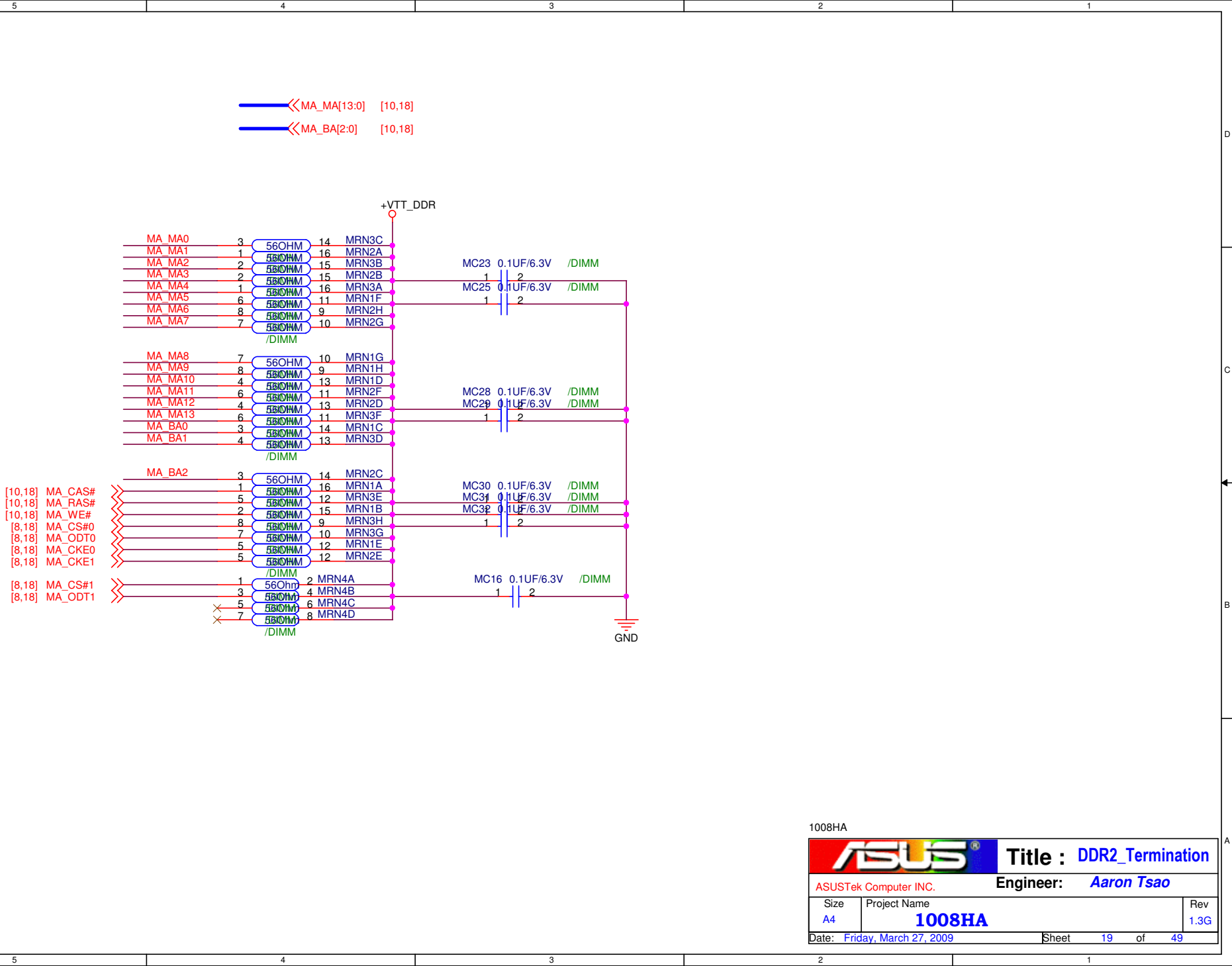
DDR2_DIMM_200P
/DIMM

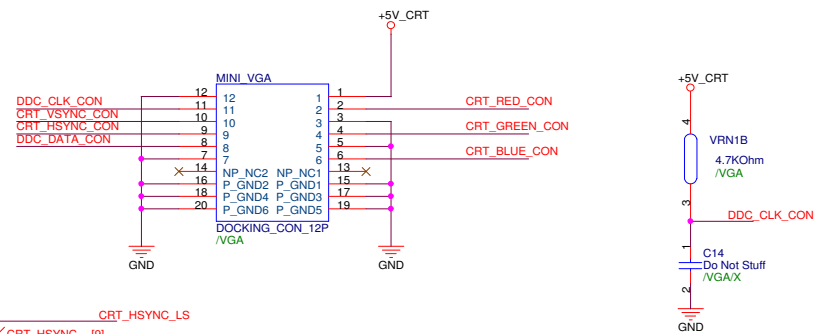
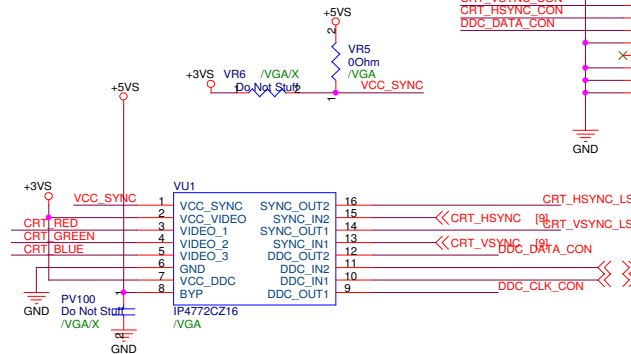
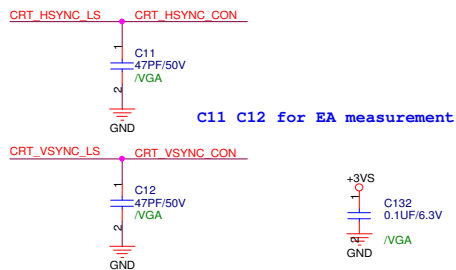
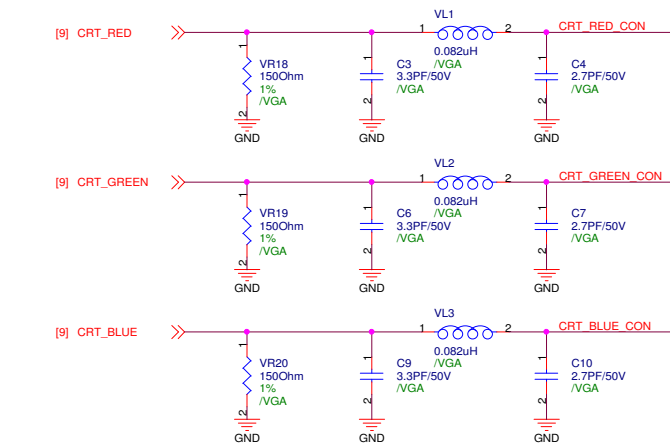
R1.1G MC3 MC4 change to 0603 1uF

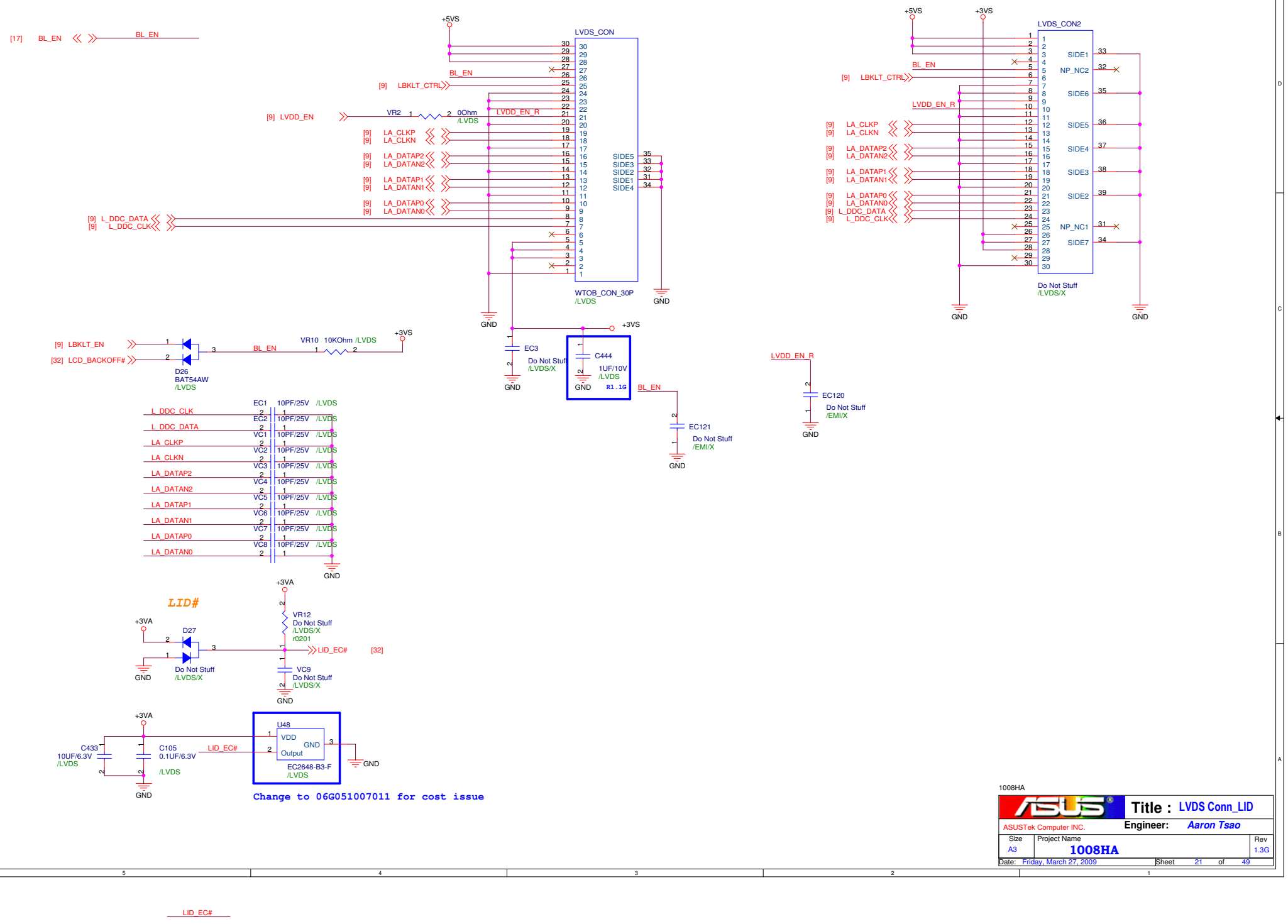


1008HA

ASUS		Title : DDR2 SODIMM	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009		Sheet 18	of 49







1008HA

3.5G Module & External Antenna



Title :

ASUSTek Computer INC.

Engineer: *Aaron Tsao*

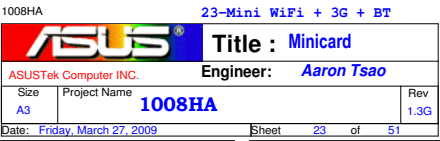
Size
A3

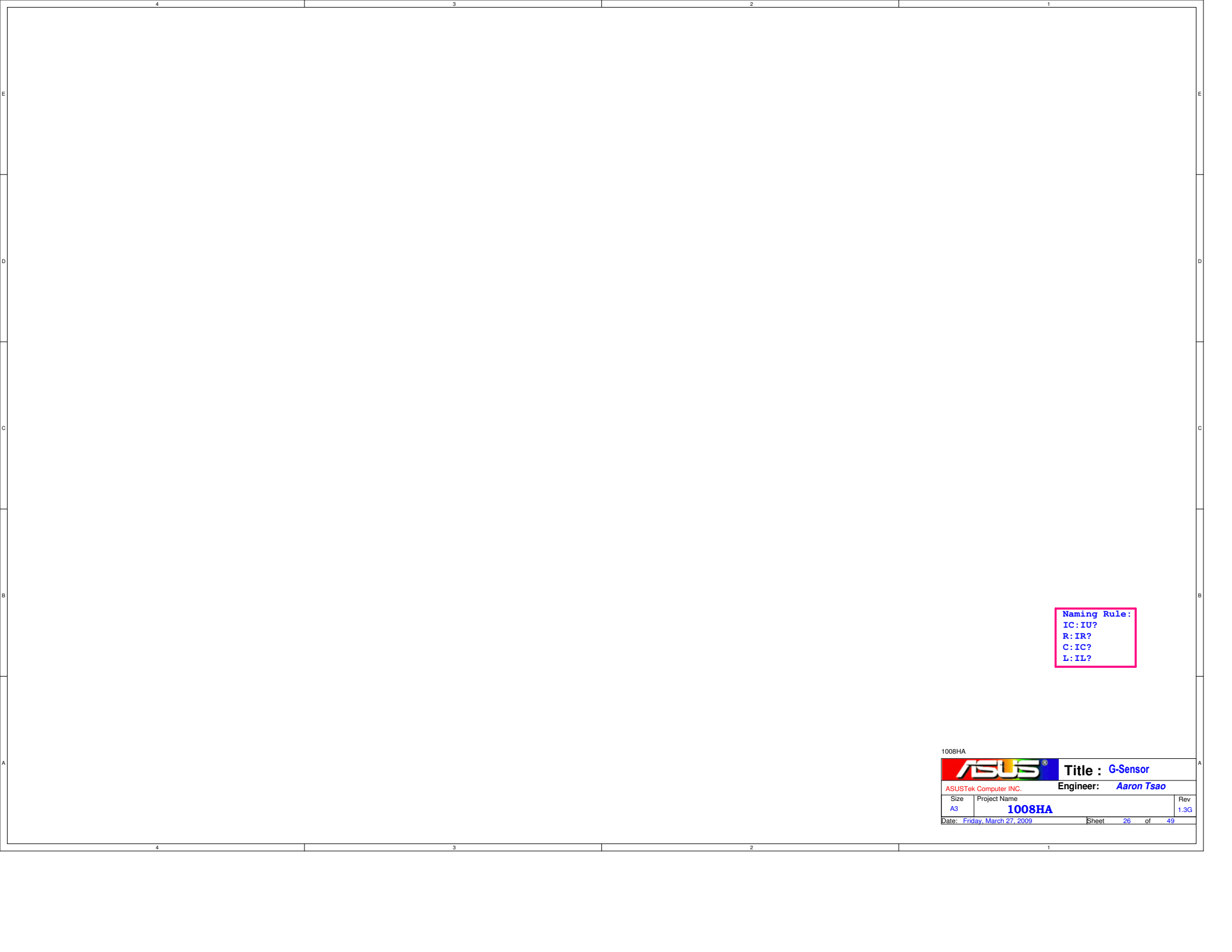
Project Name	10
--------------	----

Rev
1.3G

Date: Friday, March 27, 2009

Sheet 22 of 49





Naming Rule:
IC: IU?
R: IR?
C: IC?
L: IL?

1008HA



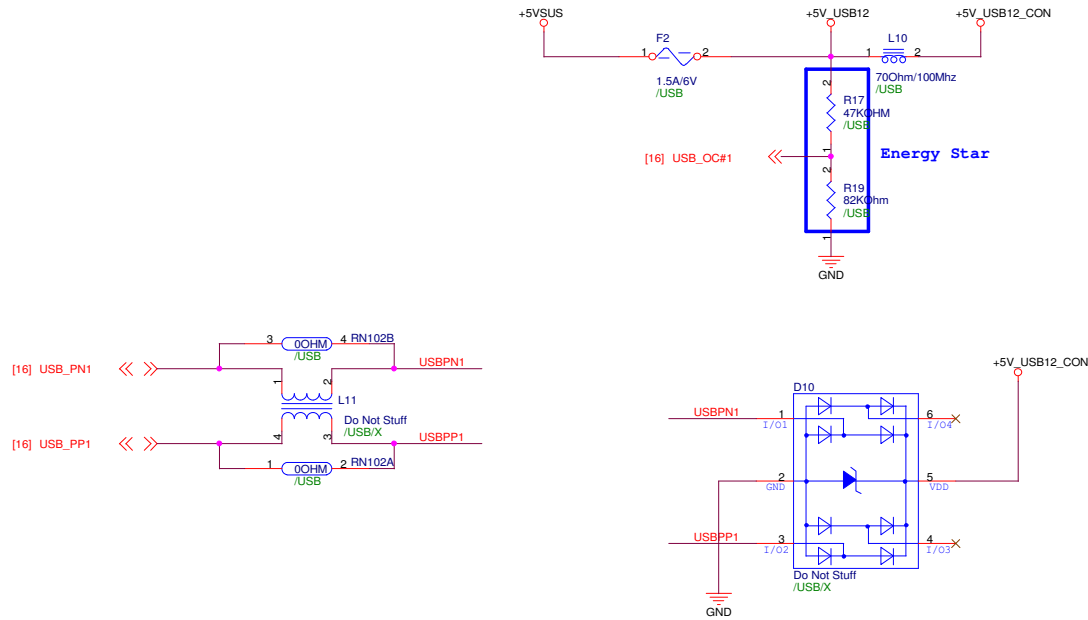
Title : G-Sensor

ASUSTek Computer INC.

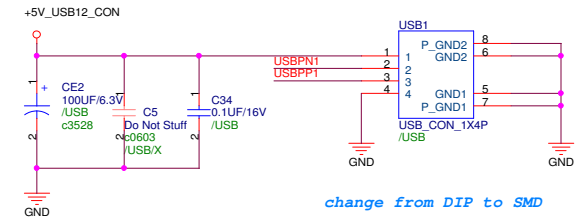
Engineer: Aaron Tsao

Size	Project Name	Rev
A3	1008HA	1.3G

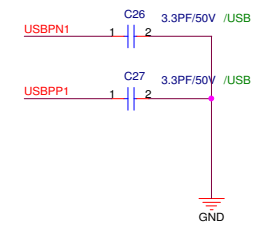
Date: Friday, March 27, 2009Sheet 26 of 49



1.1G change USB con. to 12G131030042



1.1G change CE2 CE3 CE4 to POSCAP, 100uF/6.3V



	5	4	3	2	1	
D						D
C						C
B						B
A						A
	5	4	3	2	1	

1008HA

		Title : Camera Power	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A4	Project Name 1008HA		Rev 1.3G
Date: Friday, March 27, 2009		Sheet 28	of 49

1.1G add PWR LED and Charge LED
DMIC Cable length should be less 30cm
Change R291 R292 R293 to 510 Ohm

1008HA



Title : **ALC269-2**

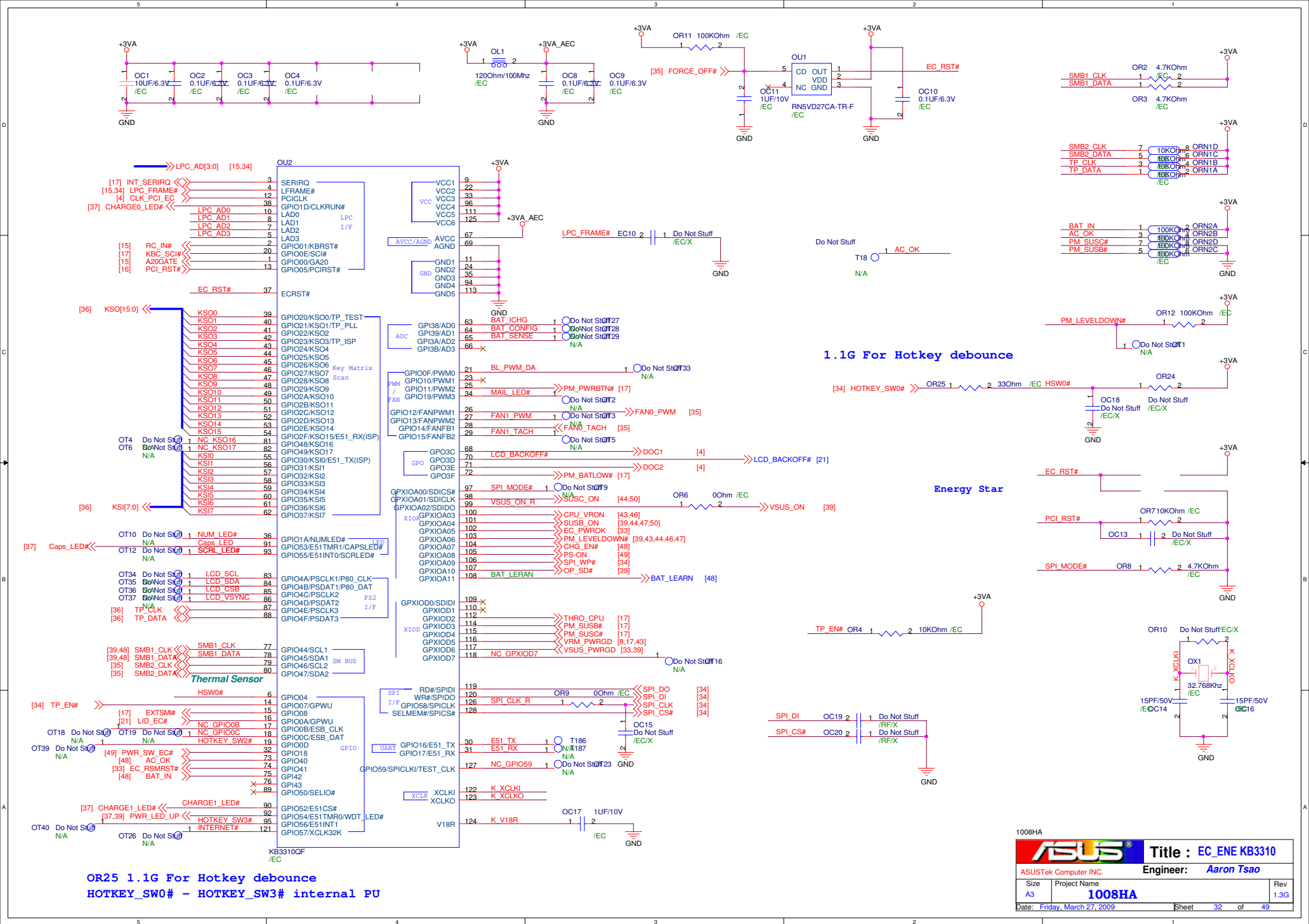
ASUSTek Computer Inc.

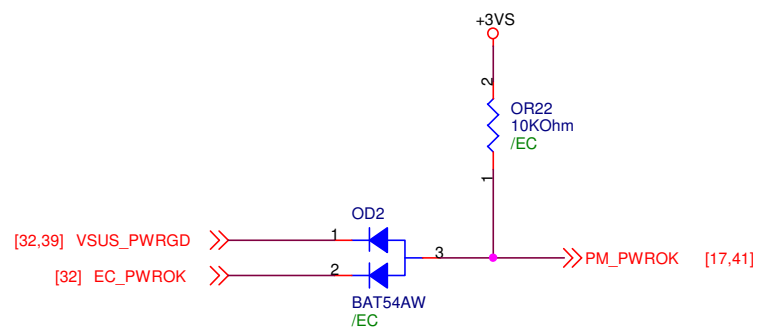
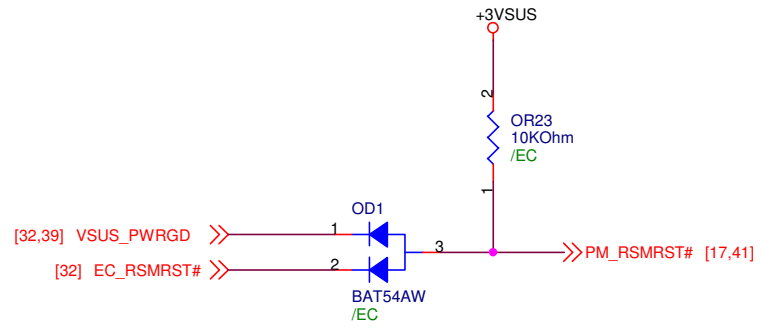
Engineer: **Aaron Tsao**

Size	Project Name	Rev
A3	1008HA	1.3G

Date: Friday, March 27, 2009

Sheet 31 of 49

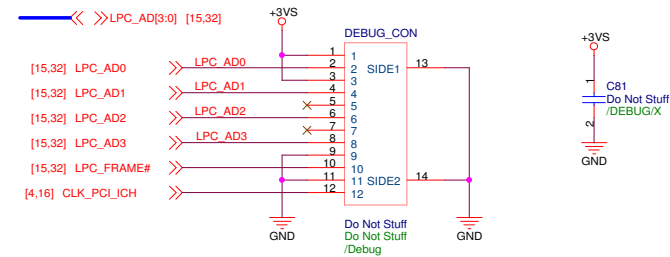




1008HA

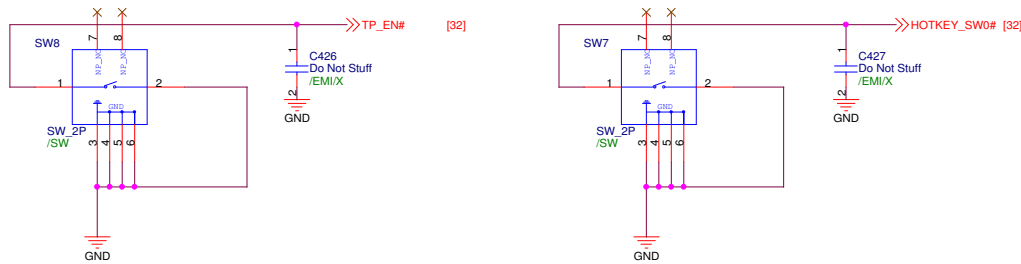
ASUS		Title : EC	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A4	Project Name 1008HA		Rev 1.3G
Date: Friday, March 27, 2009		Sheet 33 of 49	

For Debug

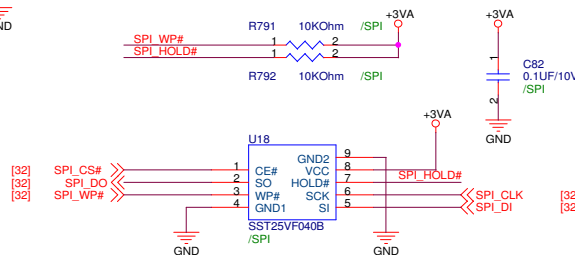
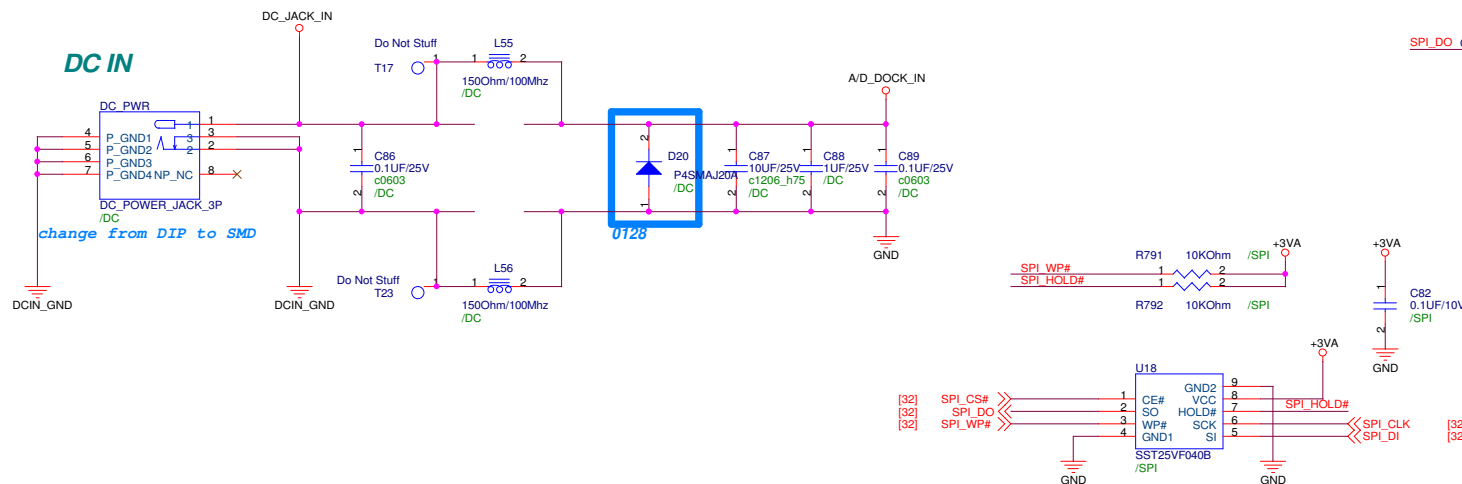


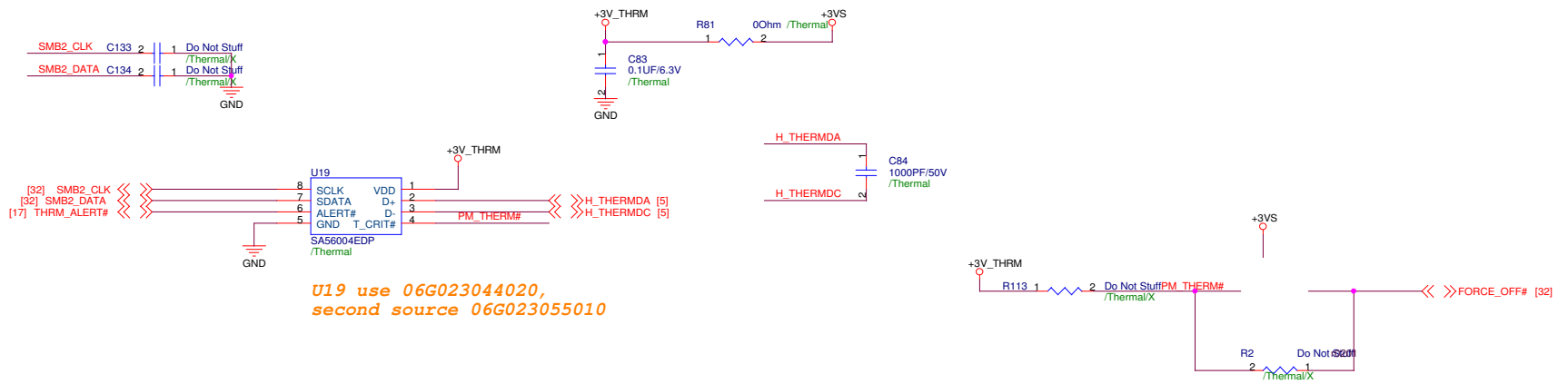
Debug Card cable use Z96 Touch Pad cable, P/N:
 14G124110126, 14G124110120, 14G124110121
 14G124110124, 14G124110125

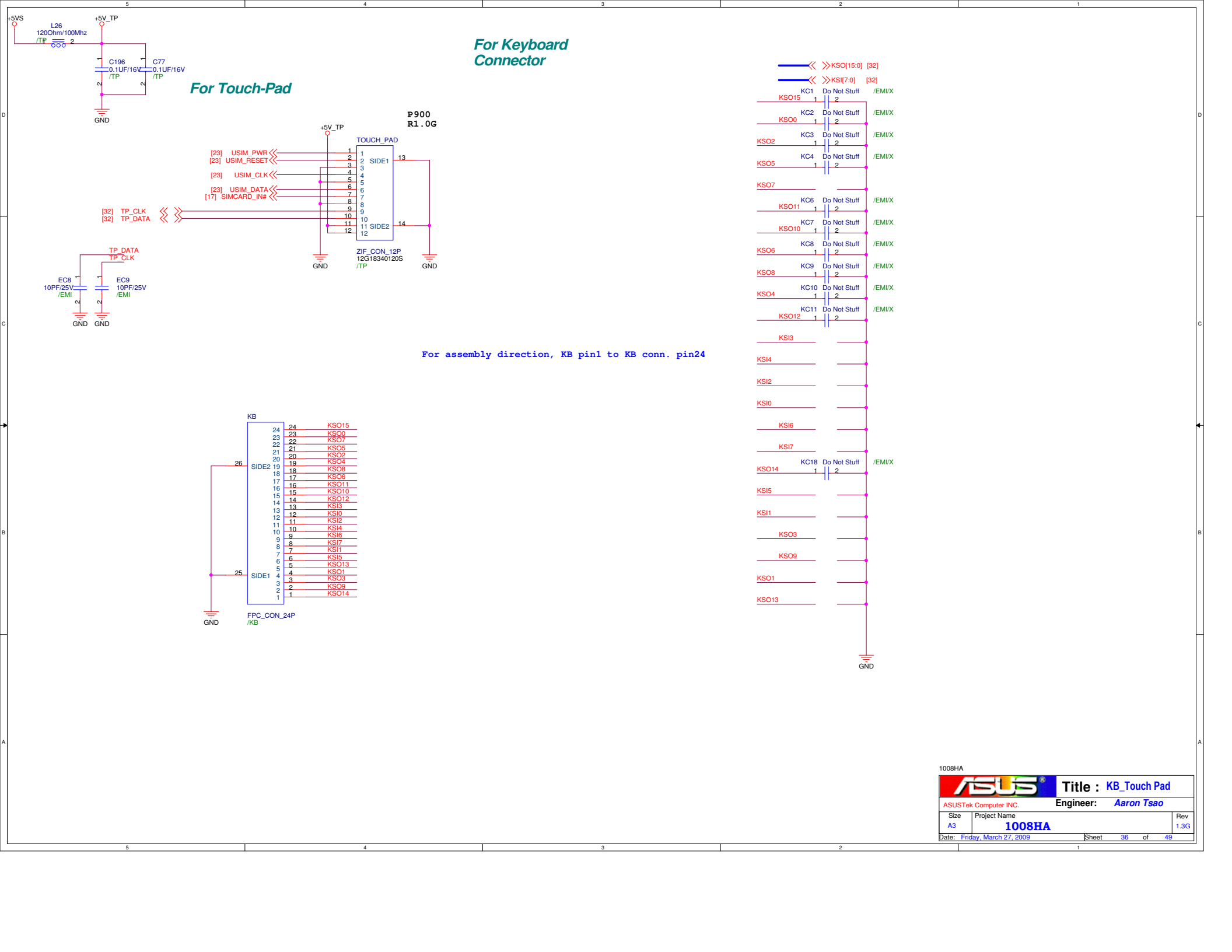
prevent system power on when LCD close



DC IN





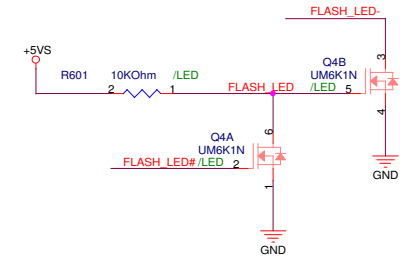
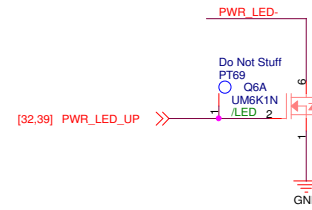
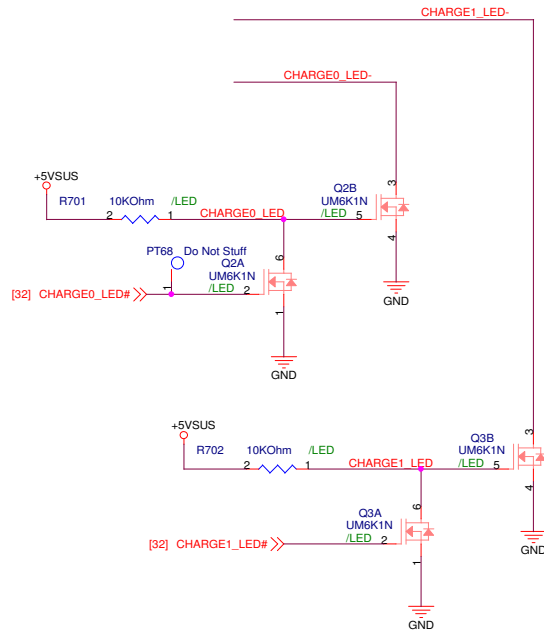


1.1G change to EVERLIGHT

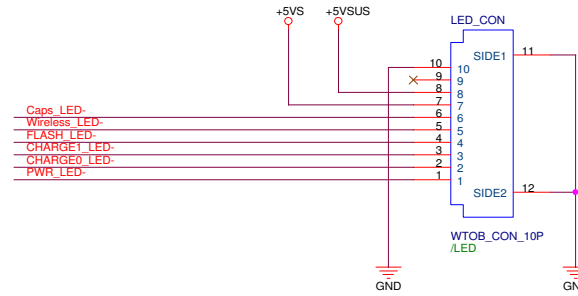
for CHARGE LED
Height : 0.55mm

for POWER LED
White

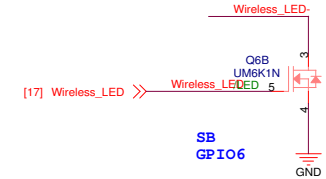
for FLASH LED
White



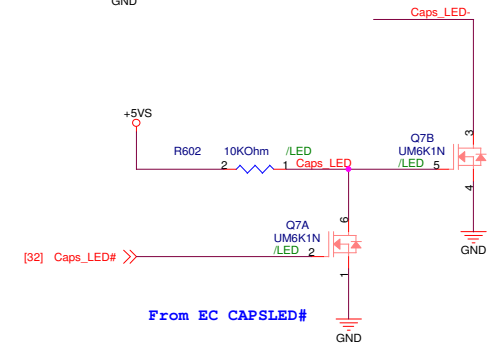
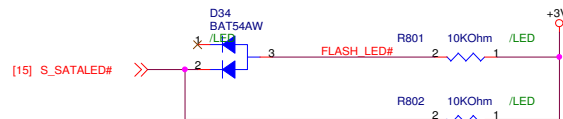
Change LED resistor to 510 Ohm, about 4mA



for WIFI/BlueTooth LED
White



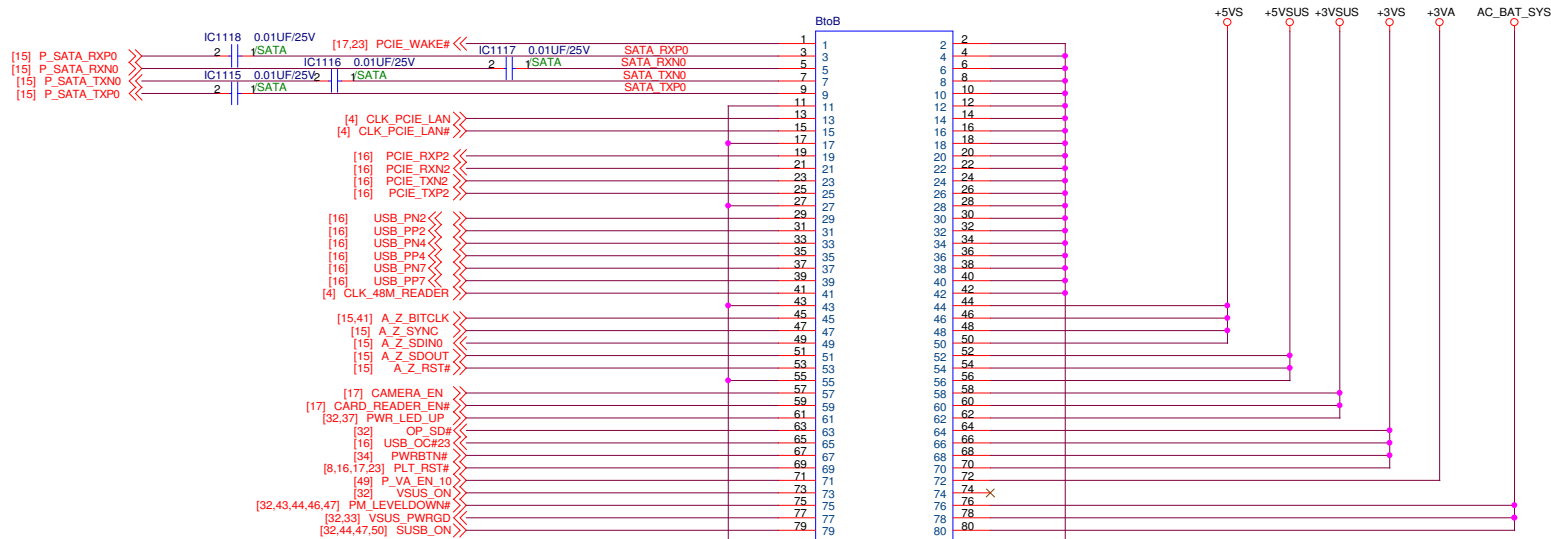
for Caps Lock LED
White



1008HA

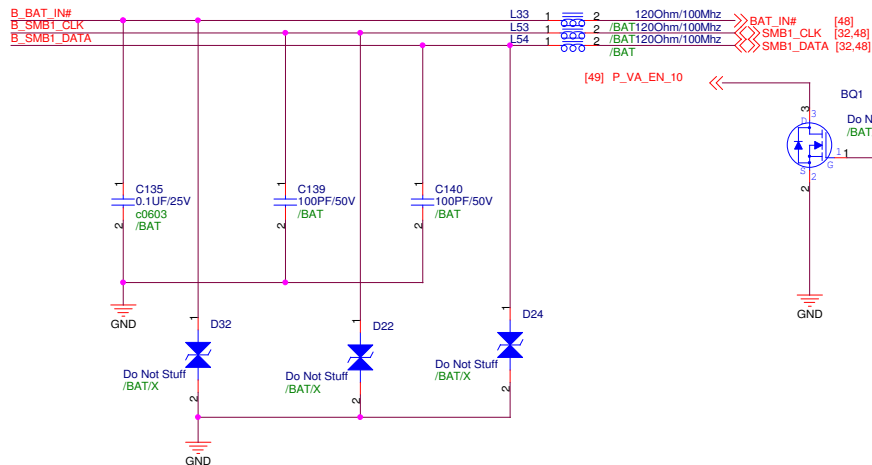
ASUS		Title : LED	
ASUSTek Computer INC.		Engineer: Kenneth Hung	
Size A3	Project Name 1008HA	Date: Friday, March 27, 2009	Rev 1.3G
Sheet 37 of 50			

PWR Board/SATA/LAN/USB Connector



Do Not Stuff
T24

L57 only for S101 2S1P and 2S2P



BAT IN

Do Not Stuff

BATT_CON

1008HA

ASUS

ASUSTek Computer INC.

Size A3

Date: Friday, March 27, 2009

Sheet 39 of 49

Rev 1.3G

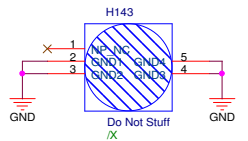
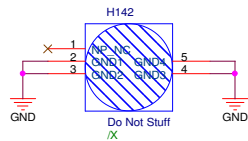
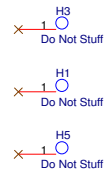
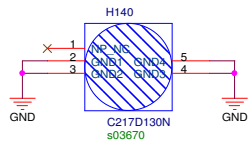
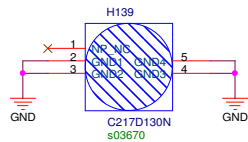
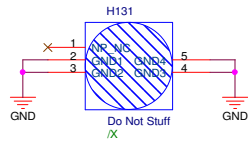
Project Name

1008HA

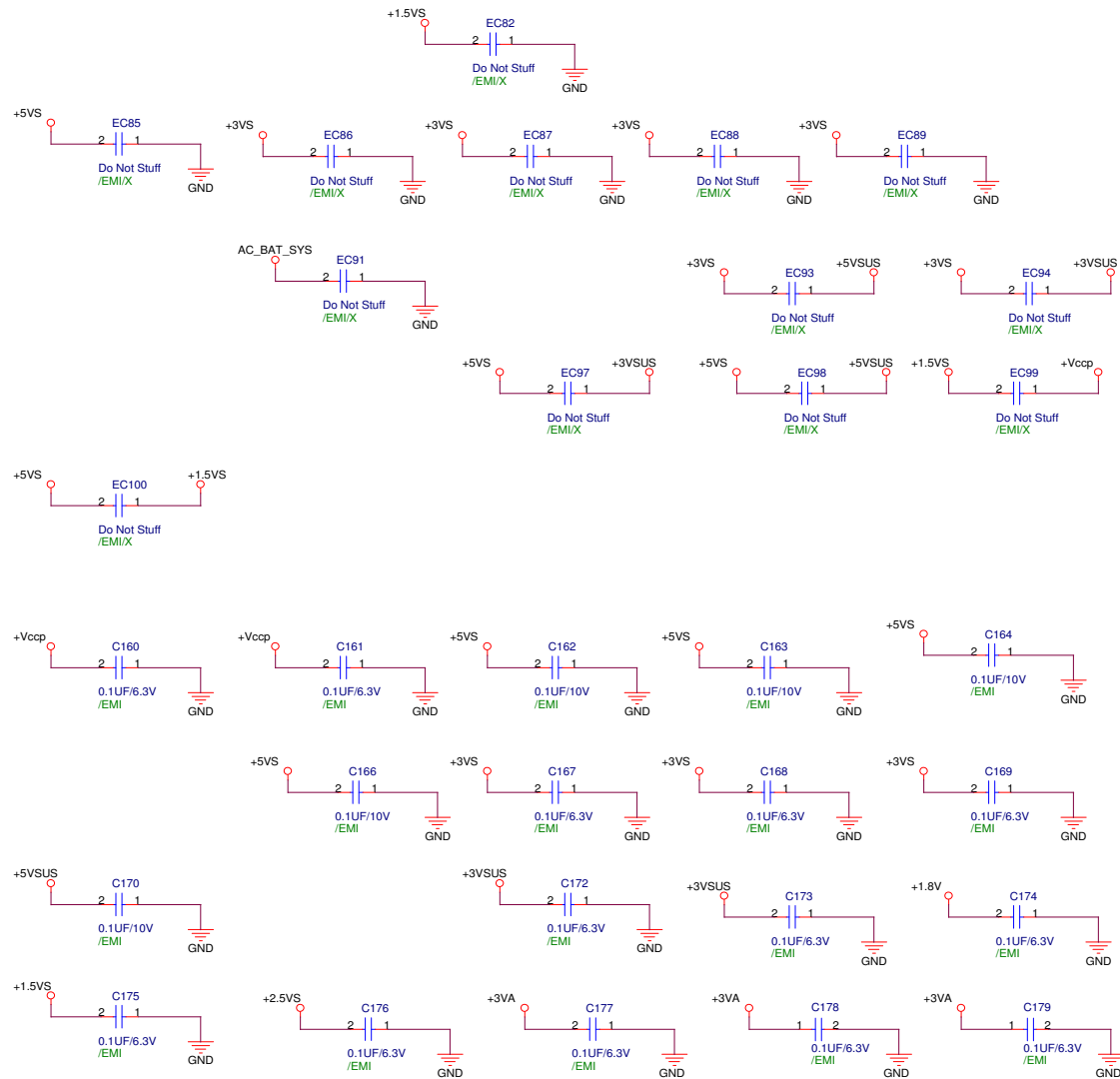
Engineer: Aaron Tsao

Title :

39 PWR Board/SATA/LAN/USB Connector

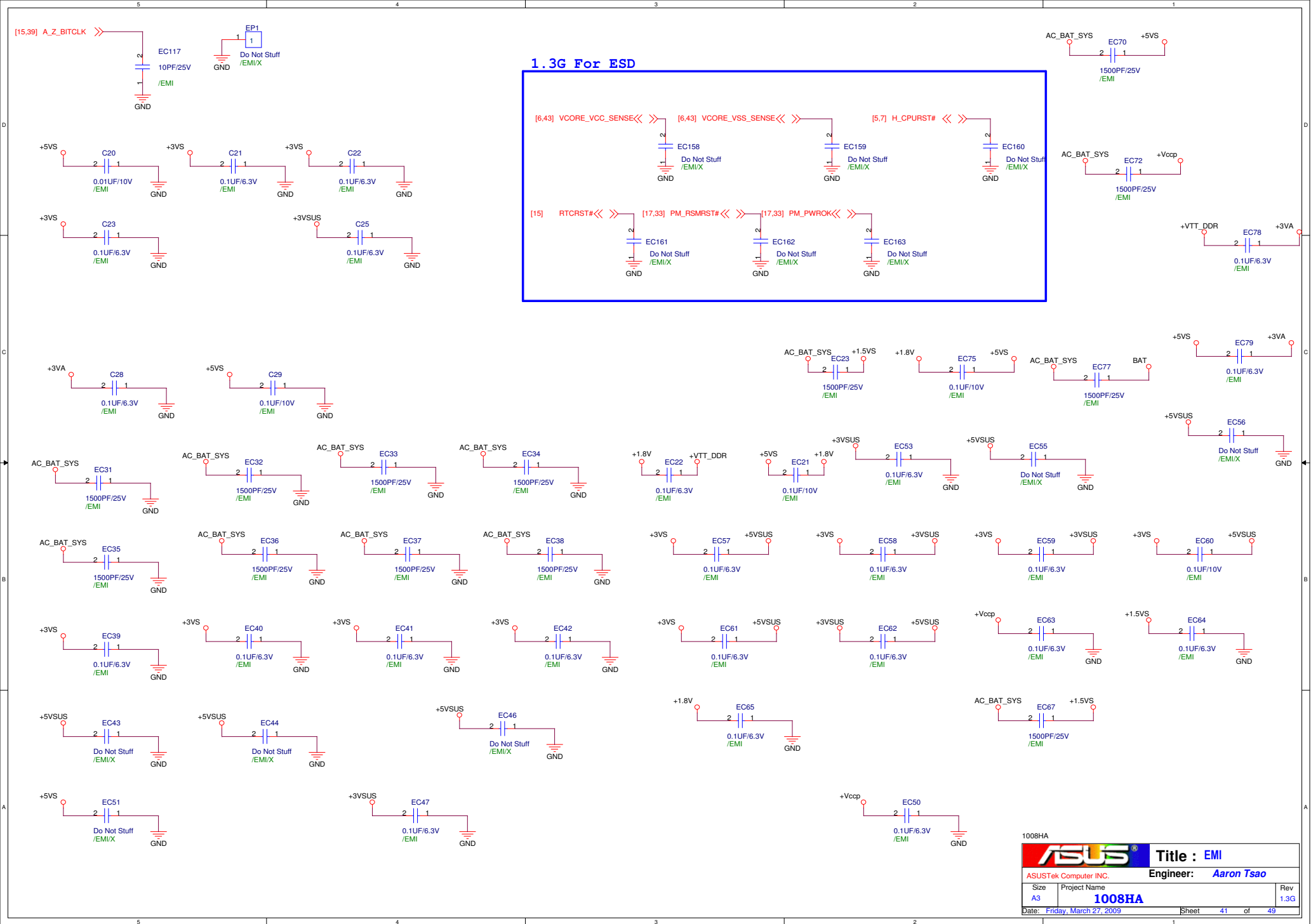


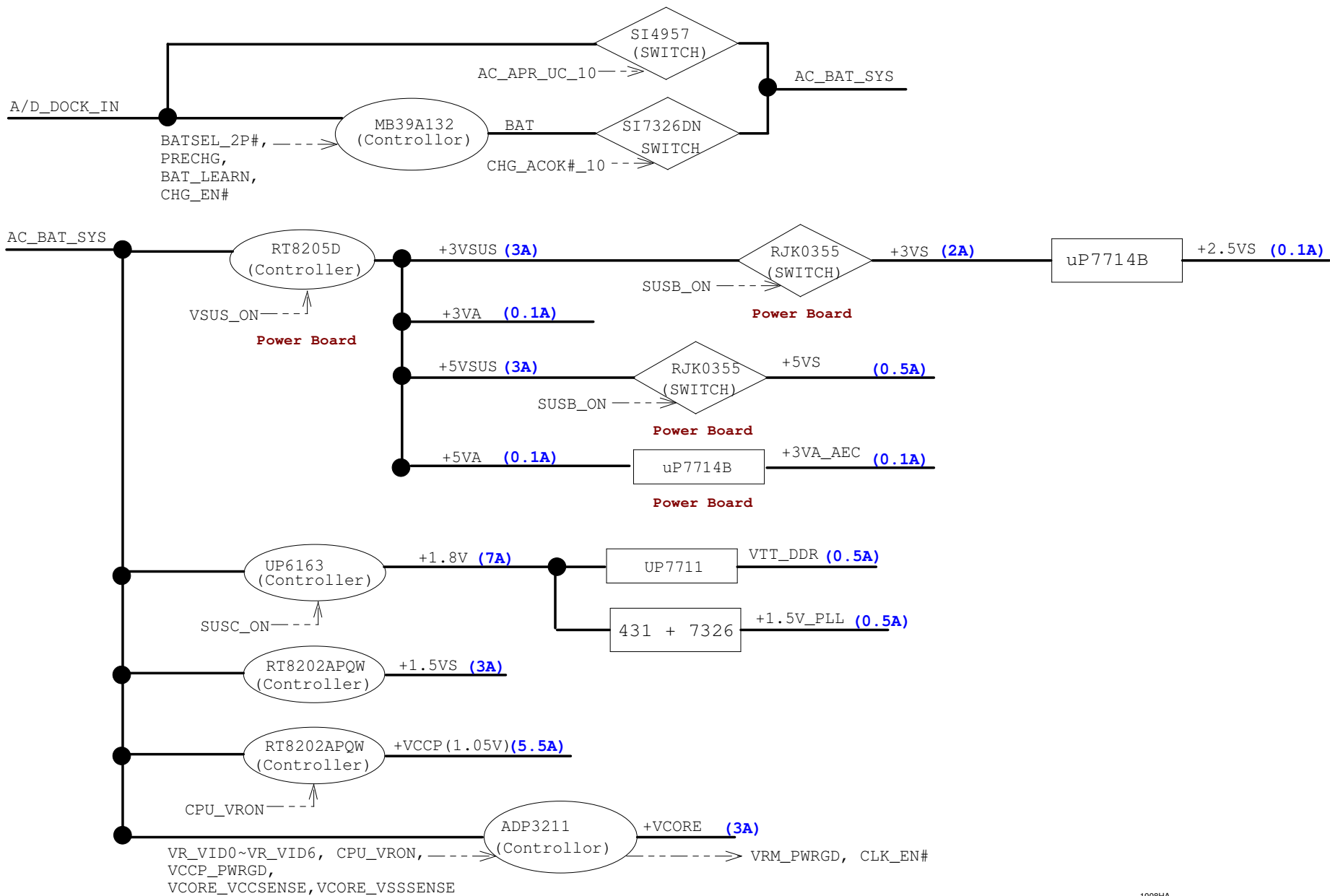
R1.1G Change



1008HA

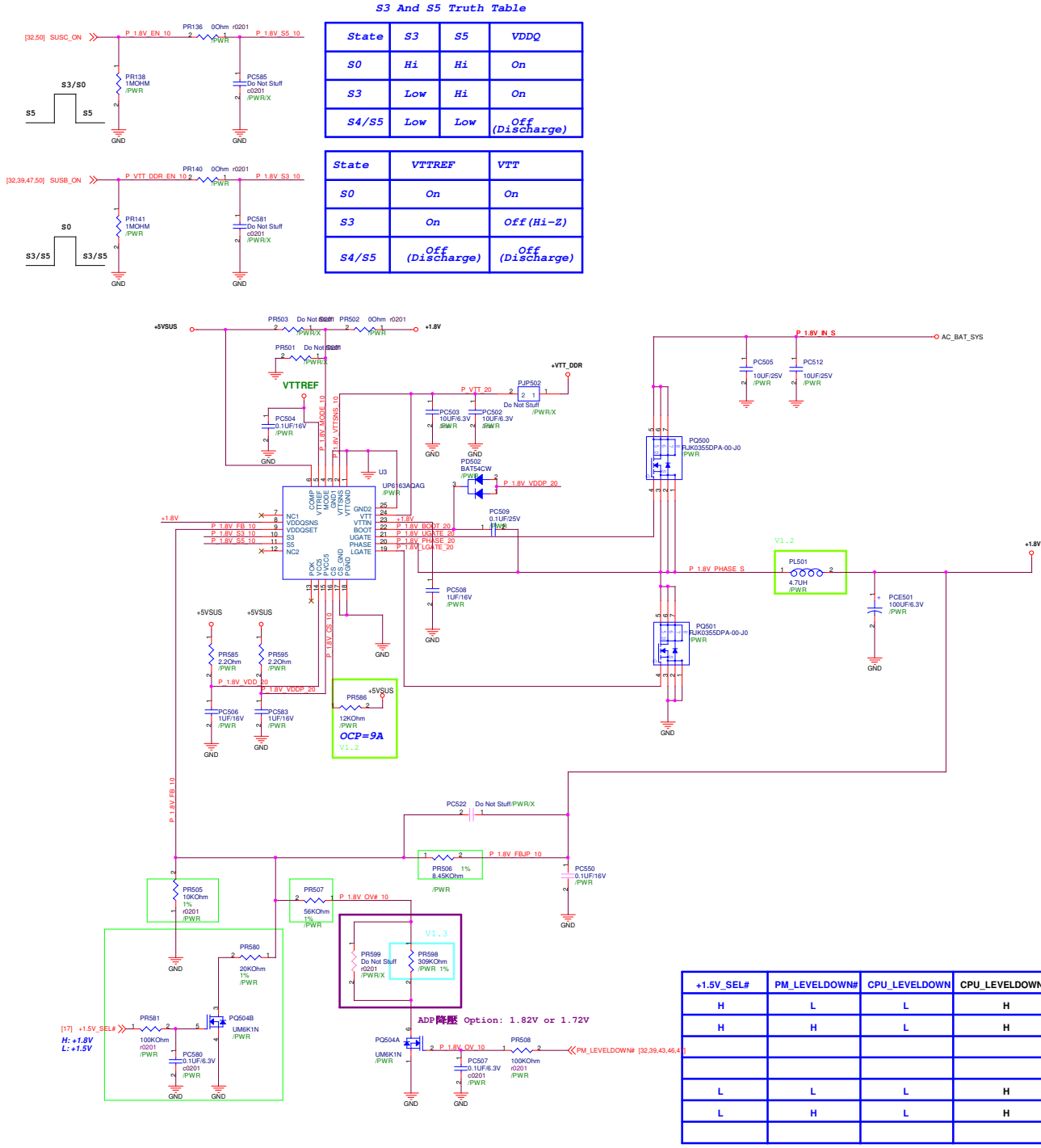
ASUS		Title : Srew Hole	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size	Project Name		Rev
A3	1008HA		1.3G
Date: Friday, March 27, 2009		Sheet	40 of 49





1008HA

ASUS		Title : Power Flow	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size A3	Project Name 1008HA	Rev 1.3G	
Date: Friday, March 27, 2009	Sheet 42 of 49		



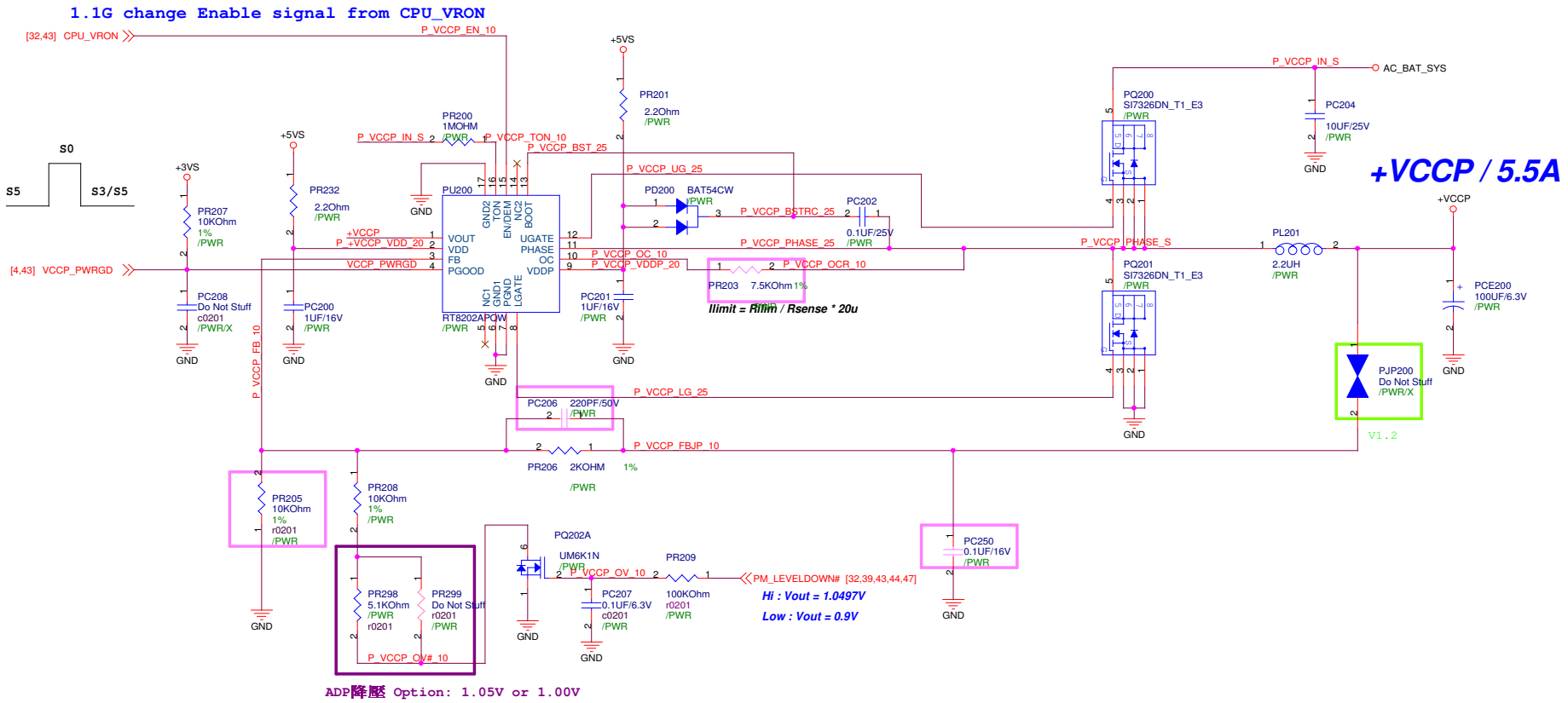
S3 And S5 Truth Table			
State	S3	S5	VDDQ
S0	Hi	Hi	On
S3	Low	Hi	On
S4/S5	Low	Low	(Off (Discharge))

State	VTTREF	VTT
S0	On	On
S3	On	Off (Hi-Z)
S4/S5	(Off (Discharge))	(Off (Discharge))

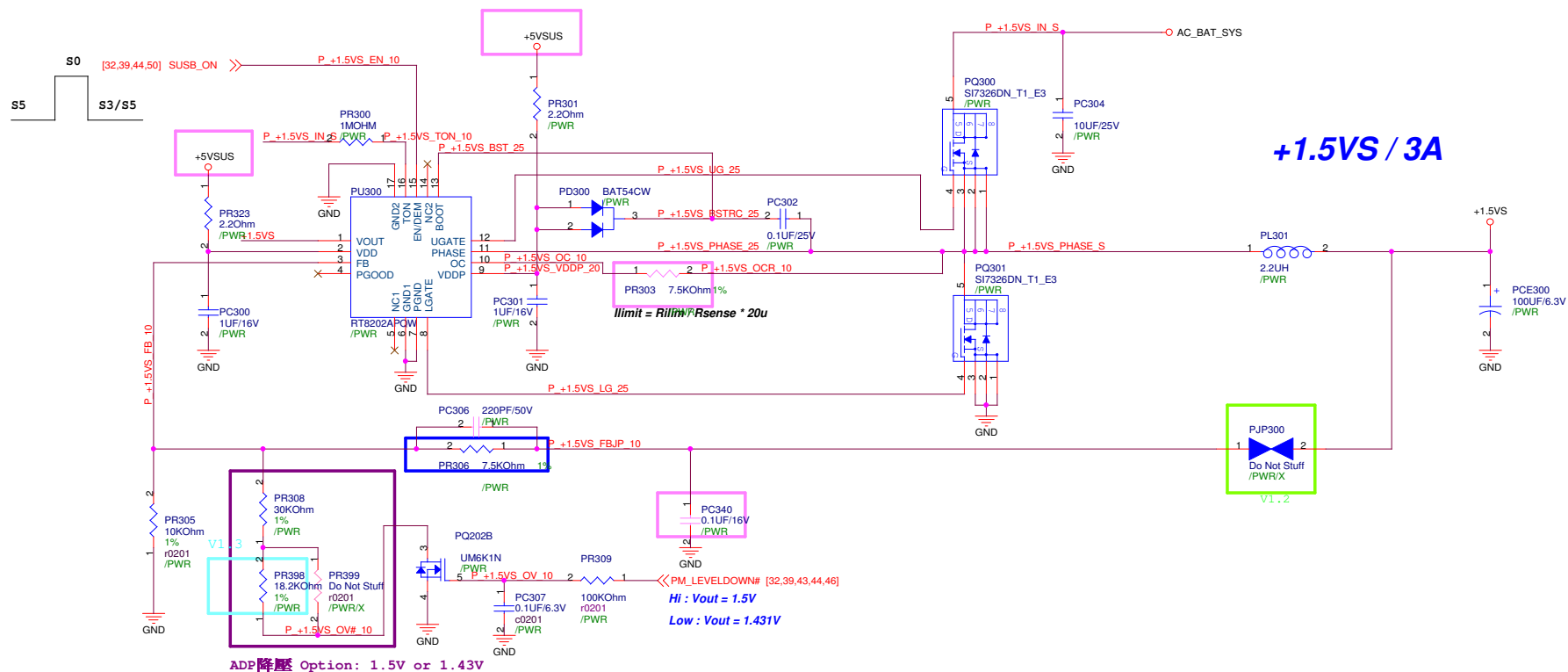
Controller	
1. Voltage & Current: +1.8V@3A	5. UVP: Vout*70%
2. Frequency: PR127=1Mohm Fosc=250KHz	6. OVP: Vout*115%
3. OCP: PR181=22Kohm ->10A	7. Enable Voltage: V rising = 2 V V falling = 0.8V
4. POR: POR Hysteresis =20mV V on =3.9-4.5V	8. Soft start time: Tss=512us
	9. Phase selection: /X
	10. Inrush Current: C total = 110 uF I inrush= 0.39 A

Power stage	
1. I/P Current: $I_{in} = V_o * I_o / (0.8 * V_{in}) = 0.94A$	5. MOSFET Spec: H-side MOSFET: AP4800AGM Rds(ON)= 21 mohm (Vgs=4.5 V) I cont = 9.6 A (T =25 °C) I peak = 40 A
2. Ripple Current: $I_{rip} = 1.78A$ $I_{spec} = 2A \times 1 \text{ pcs}$	L-side MOSFET: RAP4800AGM Rds(ON)= 21 mohm (Vgs=4.5 V) I cont = 9.6 A (T =25 °C) I peak = 40 A
3. Dynamic: $I_{peak} = 5 A$ ESR =18 mohm $\Delta V = 90mV$	
4. Inductor Spec: $I_{sat} = 10 A$ $I_{dc} = 5.5 A$ DCR=37 mohm	

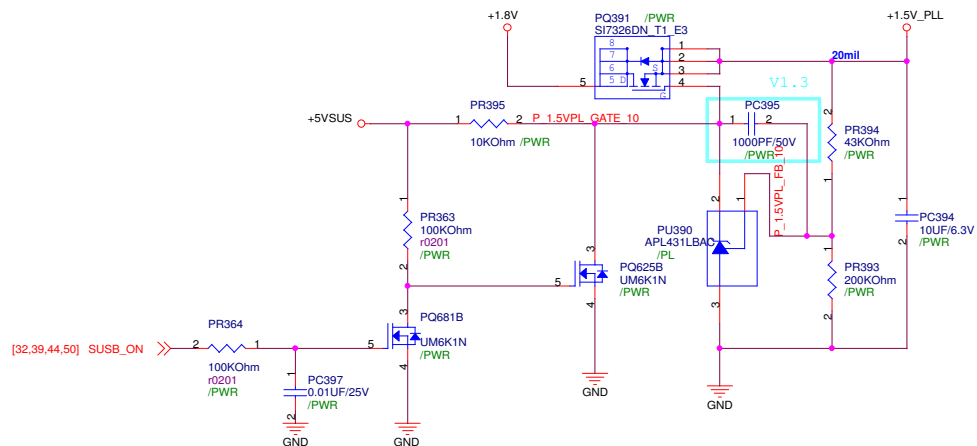
+1.5V_SEL#	PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
H	L	L	H	1.72V	Power Saving
H	H	L	H	1.82V	Normal
L	L	L	H	1.4V	Power Saving
L	H	L	H	1.5V	Normal



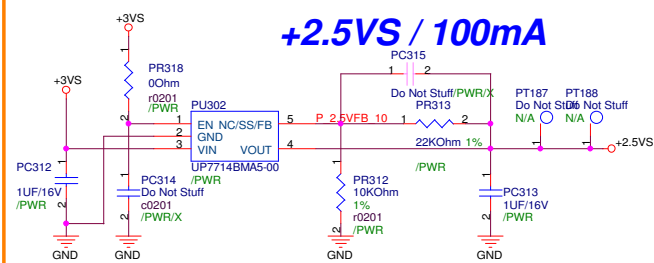
PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	0.9V	Power Saving
H	L	H	1.05V	Normal



+1.5V_PLL(1.5V)/ 0.6A

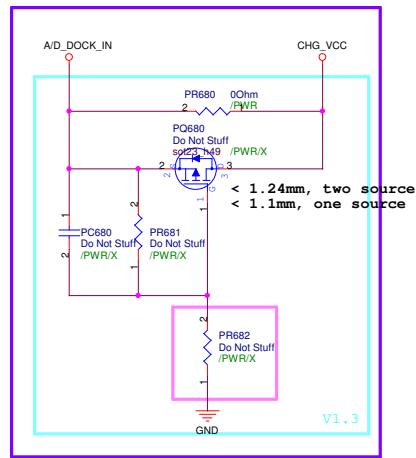


+2.5VS / 100mA



1008HA

ASUS		Title : +1.5VS & +2.5VS	
ASUSTek Computer INC.		Engineer: Aaron Tsao	
Size	Project Name	Rev	
A3	1008HA	1.3G	
Date: Friday, March 27, 2009		Sheet	47 of 51



VREF = 5.0V
 $f_{osc}(KHz) = 17000 / RT (Kohm)$
 Soft start: $ts(s) = 0.13 * CS (uF)$
 $VTH \text{ of } -IN1: 5V / 62 * (100+62) = 13.06V$
 $VTH \text{ of } ACIN: 1.25V / 25 * (185+25) = 10.5V$
 Change PR607 and PR608 value

Battery Cell Selection :

$BAT_LEARN = 1$, Battery discharges
 $BAT_ID = 1$, 2 Cells; $Vadj2 = 0.998V$
 $\Rightarrow I_{charge} = 1.477A$
 $BAT_ID = 0$, 4/6 Cells; $Vadj2 = 1.648V$
 $\Rightarrow I_{charge} = 2.517A$

Pre-Charging Mode :

Precharging current = 150mA
 $Vadj2 = 168.75mV$

Adaptor Max. Current :

$Ilimit = 1.9A$; 36.1W (19V/40W)

ACIN Threshold = 1.25V

Adaptor > 16V, System Powered by Adaptor
 Adaptor < 16V, System Powered by Battery

Battery Charging Voltage :

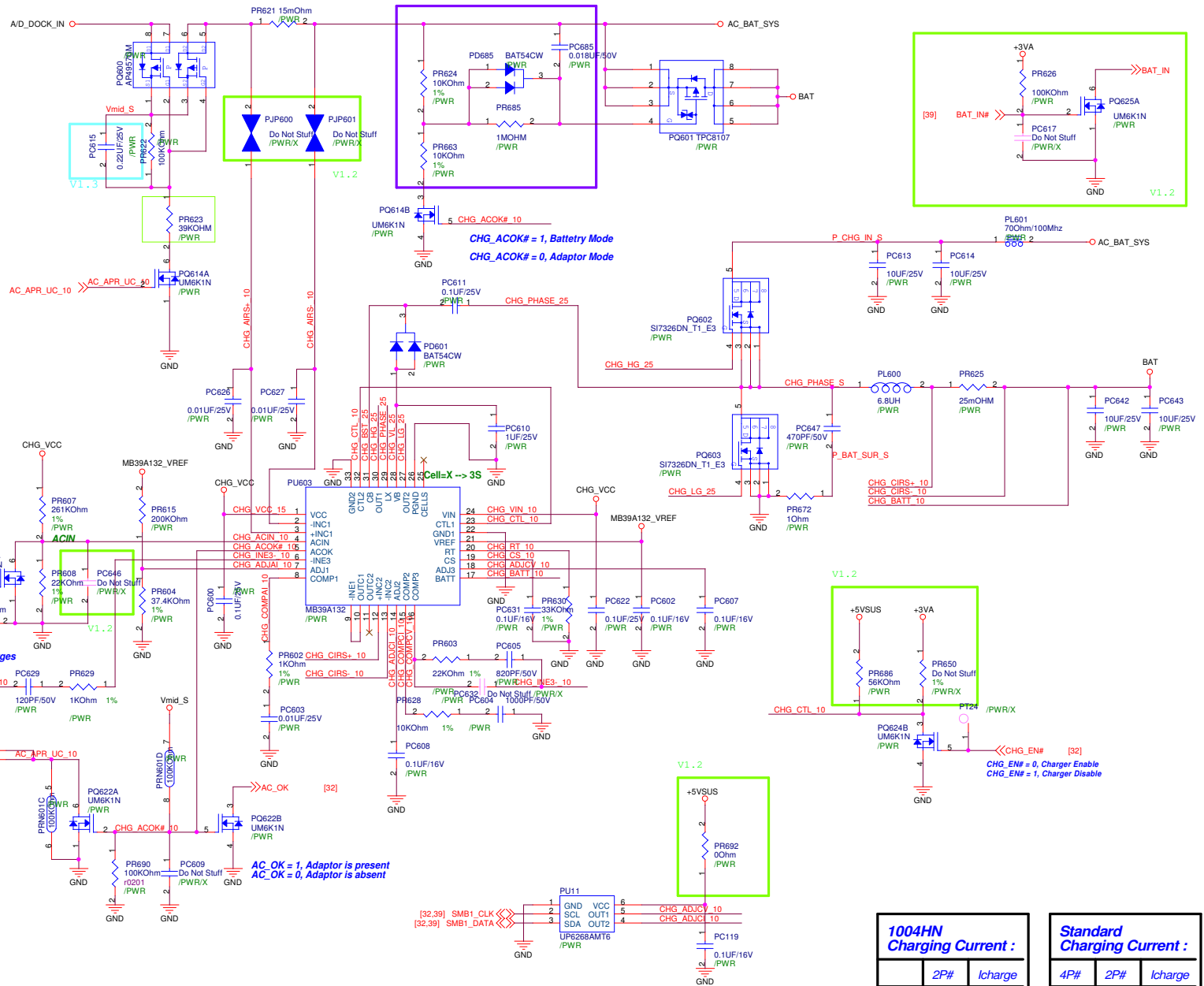
$Vadj3 > 4.1V \Rightarrow Vbat = 4.2V / cell$
 $2.2V > Vadj3 > 1.1V \Rightarrow Vbat = 2 * Vadj3 / cell$

Battery Charging Current :

$4.4V > Vadj2 \geq 0V \Rightarrow$
 $I_{chg} = (Vadj2 - 0.075) / (25 * R_s)$

Input Adaptor Max. Current Limit :

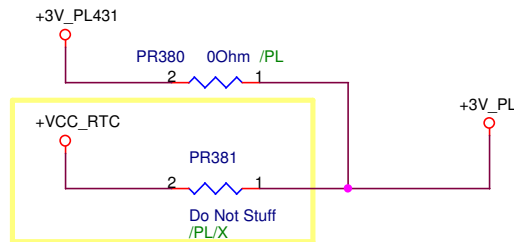
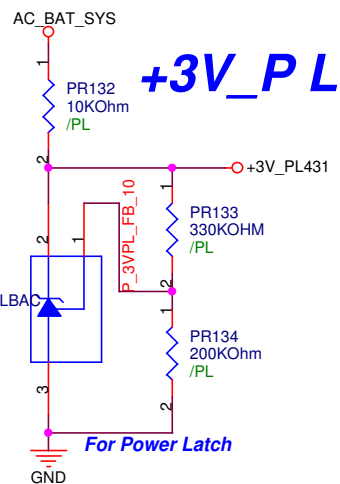
$Ilimit_current = (Vadj1 - 0.075) / (25 * R_s)$



1004HN Charging Current :		
	2P#	Icharge
	1	1.49A
	0	2.5A

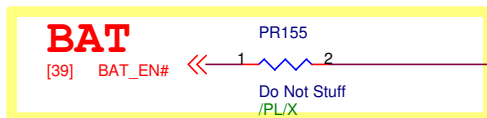
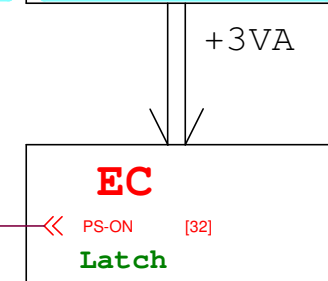
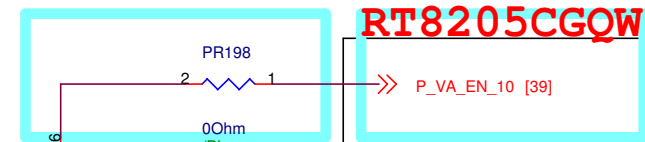
Standard Charging Current :			
	4P#	2P#	Icharge
	1	0	0.557A
	0	1	1.598A
	0	0	2.768A

1008HA

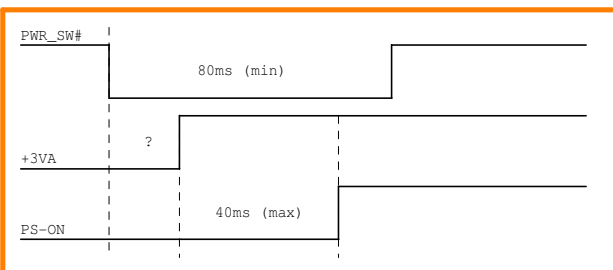
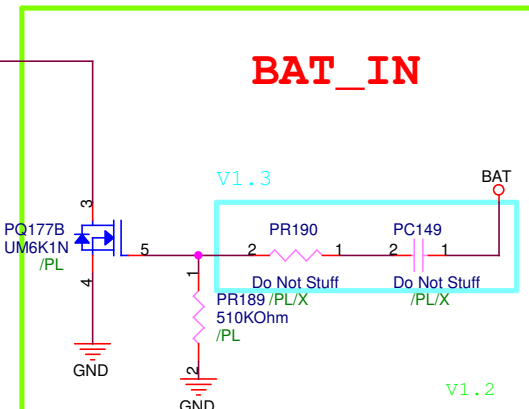
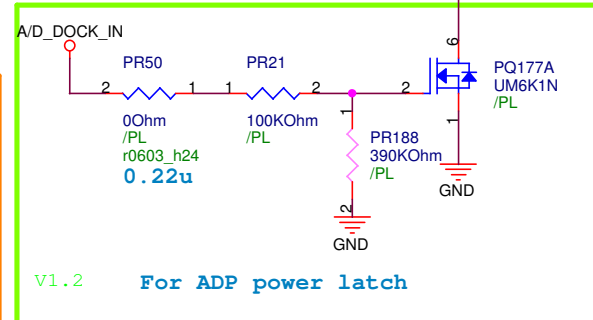


Power Latch table :

BAT	A/D_DOCK_IN		Mode
1	X	BAT /X	Latch
0	1	ADP, BAT	Out
0	0	ADP /X	Latch



ADP_IN



Rev0.7	Initial release	2008/10/22
Rev0.71	Remove hotkey switch(R2, C427, SW6, OR25)	2008/10/22
	Remove White LED (LED4, R86, Q17, SB_GPIO7)	
	Add 40 pin FPC connector	
	Change netname from BT_LED to Wireless_LED	
	Change NMOS from 2N7002 to UM6K1N(Q25, 23, Q18, Q15, Q58, Q19, Q16, Q19)	
	Change 3G power name to +3VS_3GA	
	Add dual-core support (not ready)	
Rev0.72	Change the part for LAN connector	2008/10/23
	Change the SPEAKER connector (same as FAN)	
Rev0.73	Remove CC36-40, CC45, CC24-25, CC16-17 for layout space	2008/10/27
	Remove HC32, 33 for Layout space	
	Remove HC14-16, HC125 (stuff HC126) for Layout space	
	Remove C422, 212, 232 (stuff C223) for Layout space	
	Remove C436, 292, 305 for Layout space	
	Change U47 to /X and sutff R265	
	Remove MC5, 6, 7, 9, 15, 17, 22, 24, 26, 27, 33 for Layout space	
	Remove WIFI & 3G RSV component	

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